

A Separate Compilation Extension to Standard ML (REVISED AND EXPANDED)

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Abstract

We present an extension to Standard ML, called SMLSC, to support separate compilation. The system gives meaning to individual program fragments, called units. Units may depend on one another in a way specified by the programmer. A dependency may be mediated by an interface (the type of a unit); if so, the units can be compiled separately. Otherwise, they must be compiled in sequence. We also propose a methodology for programming in SMLSC that reflects code development practice and avoids syntactic repetition of interfaces. The language is given a formal semantics, and we argue that this semantics is implementable in a variety of compilers.

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Introduction

We propose an extension to Standard ML called SMLSC. SMLSC supports separate compilation in the sense that it gives a static semantics to individual program fragments, which we call *units*. A unit may depend on other units, and can be type-checked independently of those units by specifying what it expects of them. These expectations are given in the form of *interfaces* for those other units. When unit **A** is checked against another unit **B** via a mediating interface, we need not have access to **B** at all. Therefore we say that **A** is separately compiled (SC) against **B**.

It is also useful to allow unit **A** to depend on another unit **B** without specifying an interface for **B**. In this case, the only way to derive the context necessary to check **A** is to first check **B** and read off its actual interface. In this scenario we say that **A** is incrementally compiled (IC) against **B**.

Units may be compiled and then linked together to satisfy dependencies. The compiled form of a unit or set of linked units is called a *linkset*. A linkset may be further linked with other linksets. If a linkset has no remaining dependencies, then it can be transformed into an executable program.

The goal of this work is to consolidate and synthesize previous work on compilation management for ML into a formally defined extension to the Standard ML language. The extension itself is syntactically and conceptually very simple. A unit is a series of Standard ML top-level declarations, given a name. To the current top-level declarations such as **structure** and **functor** we add an **import** declaration that is to units what the **open** declaration is to structures. An **import** declaration may optionally specify an interface for the unit, in which case we are able to compile separately against that dependency; with no interface we must compile incrementally. Compatibility with existing compilers, including whole-program compilers, is assured by making no commitment to the precise meaning of “compile” and “link”—a compiler is free to limit compilation to elaboration and type checking, and to perform code generation as part of linking.

Sections 1 and 2 summarize our main design principles, and provide an overview of the system. In Section 2 we give a small example of its use. (We give a larger example in Appendix G.) The semantics, formulated in the framework of the Harper-Stone semantics of ML [12, 13], is given in Section 3. We give an alternative semantics in the framework of *The Definition of Standard ML* [16] in Section 4. Some implementation issues are discussed in Section 5. We conclude with a discussion of related work in Section 7.

1 Design Principles

A language, not a tool. We propose an extension to the Standard ML language to support separate compilation, rather than a tool to implement it. The extension is defined by a semantics that extends the semantics of Standard ML to provide a declarative description of the meanings of the language constructs. The semantics provides a clear correctness criterion for implementations to ensure source-level compatibility among them.

Flexibility. A compilation unit consists of any sequence of top-level declarations, including signature and functor declarations.¹ However, since Standard ML lacks syntactically expressible principle signatures, some units cannot be separately compiled from one another. We therefore support incremental, as well as separate, compilation for any unit. This means that the interface of a unit can either be inferred from its source (incremental compilation) or explicitly specified (separate compilation) at the programmer’s discretion.

¹Consequently, units cannot be identified with Standard ML structures.

Simplicity. The design provides only the minimum functionality of a separate compilation system. It omits any form of compilation parameters, conditional compilation directives, or compiler directives. We leave for future work the specification of such additional machinery.

Conservativity. The semantics of Standard ML should not be changed by the introduction of separate compilation. In particular, we do not permit “circular dependencies” or similar concepts that are not otherwise expressible in Standard ML. This ensures that compilers should not be disturbed by the proposed extension beyond what is required to implement the extension itself.

Explicit dependencies. The dependencies among units are explicitly specified, not inferred. The chief reason for this is that dependencies among units may not be syntactically evident—for example, the side effects of one unit may influence the behavior of another. There are in general many ways to order effects consistently with syntactic dependencies, and these orderings need not be equivalent. A lesser reason is that supporting dependency inference requires restrictions on compilation units that are not semantically necessary, reducing flexibility.

Environment independence. The separate compilation system is defined independently of any environment in which it might be implemented. The design speaks in terms of linguistic and semantic entities, rather than implementation-specific concepts such as files or directories.

Separation of units from modules. The separate compilation system is designed as a proper extension to Standard ML so as to ensure backward compatibility of source code. It is tempting to identify compilation units with modules, but to do so would require that functors, signatures, and fixity declarations be permitted as components of modules. Permitting such an extension is not entirely straightforward; for example, permitting signature declarations in modules and their types can lead to undecidability of type checking [10].

2 Overview

Units and Interfaces

The SMLSC extension is organized around the concept of a *unit*. A unit consists of top-level declarations, which include declarations of signatures, structures, and functors. Each unit is given a name by which the unit is known throughout the program. One unit may refer to the components of another using an `import` declaration, which records the dependency of the importing unit on the imported unit, and opens it for use within the importing unit. This is the only means by which one unit may refer to another; we do not support “dot notation” for accessing the components of a unit. An `import` declaration is a new form of top-level declaration. (This is the only modification that we make to an existing syntactic category of Standard ML.)

The compilation context for a unit is entirely determined by its imports. That is, all dependencies of a unit on another unit must be explicitly indicated using `import` declarations. The dependency of one unit on another is mediated by an *interface*, the type of a unit. The interface of an imported unit can be specified in one of two ways, either *implicitly* or *explicitly*, corresponding to *incremental* or *separate* compilation.

An `import` declaration of the form `import unitid : intexp` specifies an explicit interface for the imported unit. This permits the importing unit to be compiled independently of the implementation of *unitid*, relying only on the specified interface. This is called *separate compilation*, or *SC* for

<i>srcunit</i>	<code>::= unit <i>unitid</i> = unit <i>topdec</i> end</code>	unit declaration
<i>topdec</i>	<code>::= import <i>impexp</i></code> <code> <i>strdec</i></code> <code> <i>sigdec</i></code> <code> <i>fundec</i></code> <code> local <i>topdec</i>₁ in <i>topdec</i>₂ end</code> <code> <i>topdec</i>₁ <i>topdec</i>₂</code>	open units structure-level declaration signature declaration functor declaration local declaration
<i>impexp</i>	<code>::= <i>unitid</i> ⟨: <i>intexp</i>⟩</code> <code> <i>impexp</i>₁ <i>impexp</i>₂</code>	open unit <i>unitid</i>
<i>intexp</i>	<code>::= intf <i>topspec</i> end</code>	interface expression
<i>topspec</i>	<code>::= spec</code> <code> functor <i>funspec</i></code> <code> <i>topspec</i>₁ <i>topspec</i>₂</code>	structure-level specification functor specification
<i>funspec</i>	<code>::= funid(<i>strid</i> : <i>sigexp</i>) : <i>sigexp</i>' ⟨and <i>funspec</i>⟩</code>	

Figure 1: SMLSC concrete syntax

short. An import declaration of the form `import unitid` specifies that the interface for *unitid* is to be inferred from its source code. This is called *incremental compilation*, or *IC* for short.

The concrete syntax for units and interfaces in SMLSC is given in Figure 1. We extend *topdec*s to add `import` and `local`. The `import` declaration (like `open`) allows multiple units to be simultaneously imported. Interfaces are *topspecs*; this is the syntactic class *spec* of Standard ML, with the addition of a specification form for functors. The `local` declaration limits the scope of imports, just as the structure declaration of the same name.

Projects and Linksets

A *linkset* consists of several compiled units, called its exports, together with the names and interfaces of its imports, the units on which it depends (following Cardelli [5]). A *project* consists of a linearly ordered sequence of source units and linksets. The ordering of the components in a project is significant, both because it specifies the order of identifier resolution, and because it specifies the order of computational effects when executed. Compilation of a project consists of processing the source units in the specified order to obtain linksets, and then knitting them together to resolve dependencies.

Linking consists of resolving inter-unit dependencies by binding exports to imports among linksets. When all references have been resolved, the resulting linkset can be completed to form an executable.

We do not give a concrete syntax for linksets, as we do not intend for programmers to write them, nor do we expect compatibility of linksets across implementations. Rather, they are left as implementation-specific concepts (such as object files), which are modeled here by the abstract semantic objects described in Sections 3 and 4.

Examples

We begin with a few simple examples to illustrate the features of the system.

Suppose that we have a library of data structures whose name is `Collections`. It is natural to place this library in a unit. Let's assume it contains only the queue data structure:

```

unit Collections =
unit
  signature QUEUE =
  sig
    type 'a queue
    val empty : 'a queue
    val push : 'a * 'a queue -> 'a
    val pop : 'a queue -> 'a * 'a queue
  end

  structure Queue :> QUEUE =
    struct (* ... *) end
end

```

A client of the Collections library can import it using IC easily:

```

unit Scheduler =
unit
  import Collections

  structure Sched =
    struct
      type job = (* ... *)
      val readyqueue =
        ref Queue.empty : job Queue.queue ref
      (* ... *)
    end
end

```

In these examples we use *link* to stand for the semantic operation of compiling and linking a list of source units and linksets. We can compile and link this program as

$$L = \text{link}(\text{Collections}, \text{Scheduler})$$

or we can compile the library and then the client

$$\begin{aligned}
L_0 &= \text{link}(\text{Collections}) \\
L_1 &= \text{link}(L_0, \text{Scheduler})
\end{aligned}$$

but the Scheduler unit may not be compiled on its own.

Incremental compilation is convenient when we have source or a compiled linkset for the **Collections** unit. We may prefer to use separate compilation, or may be forced to because the implementation for **Collections** is not available. A client with an SC import looks like this:

```

unit Scheduler2 =
unit
  import Collections :
  intf
    structure Queue :
      sig
        type 'a queue
        val empty : 'a queue
        val push : 'a * 'a queue -> 'a
        val pop : 'a queue -> 'a * 'a queue
      end
    end
  end

  structure Sched =
  struct
    type job = (* ... *)
    val readyqueue =
      ref Queue.empty : job Queue.queue ref
    (* ... *)
  end
end

```

This allows `Scheduler2` and `Collections` to be compiled separately:

$$\begin{aligned}
L_0 &= \text{link}(\text{Scheduler2}) \\
L_1 &= \text{link}(\text{Collections}) \\
L_2 &= \text{link}(L_1, L_0)
\end{aligned}$$

However, writing the SC import this way forces an undesirable repetition of code. If more than one client uses `Collections`—which we would expect—each client repeats the interface for its import of the unit. A further problem is that this style asks the client to supply the interface of the library, but the interface of a library is usually provided by the library author, not the client. Fortunately, a combination of SC and IC allows us to use the system in a much cleaner way.

Handoff Units

A programmer who wishes his code to be available for separate compilation can provide a *handoff unit* which supplies the interface. Starting from scratch, the handoff unit contains an SC import:

```

unit Collections =
unit
  signature QUEUE =
  sig
    type 'a queue
    val empty : 'a queue
    val push : 'a * 'a queue -> 'a
    val pop : 'a queue -> 'a * 'a queue
  end
end

```



```

import CollectionsImpl :
  intf
    structure Queue : QUEUE
  end
end

```

The implementation of the collections library is imported from the unit `CollectionsImpl`.² Because the `import` declaration opens the imported unit, all of the contents of `CollectionsImpl` are available in the `Collections` unit. Clients wishing to make use of the library simply import the handoff unit using IC, avoiding the need to specify an interface, but instead sharing the common interface provided by the handoff unit.

```

unit Scheduler3 =
unit
  import Collections

  structure Sched = (* ... *)
end

```

This additionally has the benefit that the clients only need to know the name of the handoff unit, not the implementation unit. A few such clients can be linked with the handoff unit:

$$L_0 = \text{link}(\text{Collections}, \text{Scheduler3}, \text{OtherClient})$$

The result can later be linked with the implementation of the Collections library:

$$L_1 = \text{link}(\text{CollectionsImpl}, L_0)$$

Definite References

In the terminology of Harper and Pierce [11] an import of one unit in another is interpreted as a *definite reference*—that is, as a free variable that refers to a single, specific unit through an interface for it, either inferred or specified. This ensures that if two separate units import a common unit, such as a well-known library, these units share a common understanding of the abstract types exported by that unit.

In contrast, the *fully functorized style* is to λ -abstract a module over all of the modules on which it depends. Because functors may, in principle, be applied to many different arguments, its parameters are *indefinite references*. As such sharing relationships among components are lost, because they need not be true in every instance. To avoid this, one must explicitly specify the intended sharing constraints; this can be quite burdensome.

The handoff methodology in SMLSC facilitates programming with definite references. Two pieces of code that import the same unit using separate compilation may only be linked if they import that unit at equivalent interfaces. The imports are then consolidated into a single import, ensuring that type equations hold. When the same handoff unit is used to create the two imports, these interfaces will always be equivalent. In corner cases such as skew between versions of a library’s handoff unit, the programmer may manually consolidate two imports. We discuss this further in Section 6.

²If unit `CollectionsImpl` also needs signature `QUEUE`, it can be placed in its own unit `QUEUESIG` and both `CollectionsImpl` and `Collections` can incrementally compile against `QUEUESIG`. Appendix G exemplifies this approach.

3 Semantics extending the Harper-Stone semantics of ML

In this section, we give a semantics to SMLSC by extending Harper and Stone’s Typed Semantics (TS) for Standard ML [13]. At a high level the typed semantics consists of an elaboration relation from an *external language*, called TSEL, into an *internal language*, called TSIL. The external language is a slight extension of the abstract syntax of Standard ML. The internal language is a typed λ -calculus based on the Harper-Lillibridge type theory for modules [10]. Elaboration comprises type inference, pattern compilation, equality compilation, identifier resolution, and insertions of coercions for signature matching. The result of elaboration is a well-formed program in the TSIL, to which a dynamic semantics is given to provide an execution model. The semantics of SMLSC is an extension of the Harper-Stone semantics that elaborates units into linksets that can be completed for execution.

The TSIL. We begin with a brief review of the structure of the TSIL. The TSIL consists of a core level and a module level. The core level includes expressions *exp*, constructors *con*, and kinds *knd*. Kinds classify constructors. Constructors of kind Ω are types; they classify expressions. The module level includes modules *mod* and signatures *sig*, which classify modules. We write $\{\}$ to denote the empty record, and *mod.lab* to denote the projection of a component named *lab* from the structure *mod*. The semantics works mainly with modules, ultimately elaborating units to TSIL structures.

Declaration lists serve as contexts in the TSIL static semantics. A declaration list $decs = dec_1, \dots, dec_n$ declares expression ($var:con$), constructor ($var:knd(=con)$), and module ($var:sig$) variables. A structure declaration list *sdecs* has the form

$$lab_1 \triangleright dec_1, \dots, lab_n \triangleright dec_n$$

associating a label with each declaration. The structure declaration list $lab \triangleright dec, sdecs$ binds the variable declared by *dec* with scope *sdecs*. We write $[sdecs]$ to denote the signature of a structure containing fields described by *sdecs*. Variables express dependencies between components in a structure signature and may be freely alpha-varied. Labels name components for external reference and may not be renamed without changing the meaning of the signature. Consider the declaration of a structure *m* containing an opaque type component *T* and value component *X* of that type:

$$m : [T \triangleright t:\Omega, X \triangleright x:t].$$

We can systematically rename the bound variables *t* and *x*. A *path* is a module variable followed by a list of labels, serving a role similar to SML long identifiers. The paths *m.T* (a constructor) and *m.X* (an expression) refer to *m*’s components.

A *bnd* binds a variable to an expression ($var=exp$), constructor ($var=con$), or module ($var=mod$). A structure binding list *sbnds* has the form

$$lab_1 \triangleright bnd_1, \dots, lab_n \triangleright bnd_n.$$

A structure is written $[sbnds]$. The module syntax is closed under the formation of functors: dependently typed functions from modules to modules.

We shall use the TSIL judgements given in Figure 2. These judgements have the following meaning.

- $decs \vdash sdecs$ ok. No label is used twice and every declaration is well-formed. For example,

$$\vdash T \triangleright t:\Omega, X \triangleright x:t \text{ ok.}$$

<i>Judgement...</i>	<i>Meaning...</i>
$\vdash decs \text{ ok}$	$decs$ is well-formed
$decs \vdash sdecs \text{ ok}$	$sdecs$ is well-formed
$decs \vdash sig : \text{Sig}$	sig is well-formed
$decs \vdash sig \equiv sig' : \text{Sig}$	signature equivalence
$decs \vdash sbnds : sdecs$	$sbnds$ has declaration list $sdecs$
$decs \vdash mod : sig$	mod has signature sig

Figure 2: TSIL judgements (summary)

<i>Judgement...</i>	<i>Meaning...</i>
$\Gamma \vdash strdec \rightsquigarrow sbnds : sdecs$	structure declaration elaboration
$\Gamma \vdash sigexp \rightsquigarrow sig : \text{Sig}$	signature elaboration
$\Gamma \vdash spec \rightsquigarrow sdecs$	specification elaboration
$\Gamma \vdash_{\text{ctx}} labs \rightsquigarrow path : class$	context lookup
$decs \vdash_{\text{sub}} path : sig_0 \preceq sig \rightsquigarrow mod : sig'$	coercion compilation

Figure 3: TS elaboration judgements (summary)

- $decs \vdash sig : \text{Sig}$. The signature sig is well-formed.
- $decs \vdash sig \equiv sig' : \text{Sig}$. The signatures sig and sig' declare the same components, in the same order, with the same labels, and corresponding type components are equivalent.
- $decs \vdash sbnds : sdecs$. The structure binding list $sbnds$ matches the structure declaration list $sdecs$. Corresponding labels must agree and each bound expression, constructor, or module in $sbnds$ must match its declaration in $sdecs$. For example, the judgement

$$decs \vdash (lab \triangleright var = mod, sbnds) : (lab \triangleright var : sig, sdecs)$$

holds if $decs \vdash mod : sig$ and $decs, var : sig \vdash sbnds : sdecs$.

- $decs \vdash mod : sig$. The module mod has signature sig . The signature sig may or may not be fully transparent. For example, we may derive both

$$m : [T \triangleright t : \Omega, X \triangleright x : t] \vdash m : [T \triangleright t : \Omega = m.T, X \triangleright x : t]$$

and

$$m : [T \triangleright t : \Omega, X \triangleright x : t] \vdash m : [T \triangleright t : \Omega, X \triangleright x : t].$$

The former signature is said to be *selfified* with respect to the variable m .

TS elaboration. Harper and Stone give a semantics to Standard ML by elaboration of TSEL into TSIL. Elaboration is performed in a context Γ consisting of a structure declaration list ($sdecs$) that, due to shadowing, may have duplicate labels. We shall use the TS elaboration judgements given in Figure 3. These judgements have the following meaning.

- $\Gamma \vdash \text{strdec} \rightsquigarrow \text{sbnds} : \text{sdecs}$. Elaborate the TSEL structure declaration strdec to the structure binding list $\text{sbnds} : \text{sdecs}$. Since the TSEL permits functors within structures, this includes elaboration of functor declarations.
- $\Gamma \vdash \text{sigexp} \rightsquigarrow \text{sig} : \text{Sig}$. Elaborate the TSEL signature expression sigexp to the signature sig . The TSEL does not include signature declarations; we treat them as abbreviations for TSIL signatures, recording them in linksets and expanding them during elaboration.
- $\Gamma \vdash \text{spec} \rightsquigarrow \text{sdecs}$. Elaborate the TSEL specification spec to the structure declaration list sdecs . This includes elaboration of functor specifications.
- $\Gamma \vdash_{\text{ctx}} \text{labs} \rightsquigarrow \text{path} : \text{class}$. Perform identifier resolution in the context Γ . The input is a list of labels, which is derived from an SML long identifier; the output is a path classified by the type, kind, or signature class . Some labels in the context are annotated with a star, indicating that they are “open” (in the sense of the SML `open` declaration). Identifier resolution searches Γ from right to left, descending into structures with starred labels. For example, we may derive

$$T \triangleright t_1 : \Omega, T \triangleright t_2 : \Omega = \{\} \vdash T \rightsquigarrow t_2 : \Omega = \{\}$$

and

$$X \triangleright x_1 : \{\}, 1^* \triangleright m : [T \triangleright t : \Omega, X \triangleright x_2 : t] \vdash X \rightsquigarrow m.X : m.T.$$

- $\text{decs} \vdash_{\text{sub}} \text{path} : \text{sig}_0 \preceq \text{sig} \rightsquigarrow \text{mod} : \text{sig}'$. Perform transparent signature ascription. The inputs are a signature sig_0 , a path having that signature, and a target signature sig . The output is a module $\text{mod} : \text{sig}'$, where sig' has the same shape as sig but is fully transparent relative to path .

Elaboration maps TSEL identifiers to TSIL labels using a function $\bar{\cdot}$. To implement identifier “shadowing,” elaboration employs a function $\text{sbnds} ++ \text{sbnds}' : \text{sdecs} ++ \text{sdecs}'$ that concatenates $\text{sbnds} : \text{sdecs}$ and $\text{sbnds}' : \text{sdecs}'$, renaming labels in the left hand sides that appear in the right hand sides. The function chooses fresh labels that do not correspond to TSEL identifiers. For example, if

$$\begin{aligned} \text{sbnds} : \text{sdecs} &= \bar{T} \triangleright t_1 = \{\} : \bar{T} \triangleright t_1 : \Omega = \{\} \\ \text{sbnds}' : \text{sdecs}' &= \bar{T} \triangleright t_2 = \text{Int} : \bar{T} \triangleright t_2 : \Omega = \text{Int}, \end{aligned}$$

then $\text{sbnds} ++ \text{sbnds}' : \text{sdecs} ++ \text{sdecs}'$ might be

$$(\text{lab} \triangleright t_1 = \{\}, \bar{T} \triangleright t_2 = \text{Int}) : (\text{lab} \triangleright t_1 : \Omega = \{\}, \bar{T} \triangleright t_2 : \Omega = \text{Int})$$

where lab is not in the range of the $\bar{\cdot}$ function.

3.1 Linking

We define linking for the TSIL by giving rules for deriving the linking judgements in Figure 4. A linkset

$$\text{sdecs}_0 \rightarrow \text{sbnds} : \text{sdecs}; S$$

comprises imports sdecs_0 , exports $\text{sbnds} : \text{sdecs}$, and signature abbreviations S .

<i>Judgement...</i>	<i>Meaning...</i>
$decs \vdash L \text{ ok}$	L is well-formed
$decs \vdash S \text{ ok}$	S is well-formed
$L \rightsquigarrow exp : \{\}$	L completes to exp
$decs \vdash L ++ L' \rightsquigarrow L''$	L and L' merge to L''

Figure 4: Linking judgements

$L ::=$	$sdecs_0 \rightarrow sbnds : sdecs; S$	linkset
$S ::=$	\cdot	
	$S, sigid = sig$	top-level
	$S, unitid = S'$	declared by $unitid$

Figure 5: Linkset syntax

- The imports $sdecs_0$ describe the TSIL structures on which the linkset depends; they must be well-formed in the ambient context. For example, the imports

$$sdecs_{AB} = \begin{array}{l} A \triangleright a : [T \triangleright t : \Omega, X \triangleright x : t], \\ B \triangleright b : [Y \triangleright y : a.T] \end{array}$$

express dependency on structures labelled A and B .

Imports specify assumptions to be satisfied by linking. A linkset with imports $sdecs_{AB}$ assumes structure B binds (at least) a value $Y : a.T$ but can be linked with (a linkset exporting) a structure B providing more components.

- The exports $sbnds : sdecs$ are the TSIL code associated with the linkset. They may make reference to the linkset's imports. Continuing our example, the exports

$$sbnds_{ZR} : sdecs_{ZR} = \begin{array}{l} Z \triangleright z = b.Y, R \triangleright r = a.T : \\ Z \triangleright z : a.T, R \triangleright r : \Omega = a.T \end{array}$$

reference the imports $sdecs_{AB}$ to bind an expression Z of the imported type and an equivalent type R .

- The signature abbreviations S are used during elaboration. They may make reference to the linkset's imports and exports. Continuing our example, the signature abbreviations

$$S_{\text{SIG}} = \text{SIG} = [M \triangleright m : \Omega = r]$$

specify that elaboration should treat the signature identifier **SIG** as an abbreviation for a TSIL signature referencing the exported type R .

The dynamic semantics for SMLSC is very simple. The completion judgment $L \rightsquigarrow exp : \{\}$ translates a linkset

$$\cdot \rightarrow sbnds : sdecs; S$$

with no imports to a TSIL expression

$$[sbnds, lab \triangleright var = \{\}].lab : \{\}$$

where lab and var are fresh. Under the TSIL dynamic semantics, the resulting expression evaluates the linkset's exports from left to right for their side-effects. Evaluation terminates when an uncaught exception is raised or when every export has been evaluated.

We give the full syntax for linksets in Figure 5 and the rules in Appendix A. The remainder of this section explains the rules for linkset merge.

Notation. We write $decs, sdecs$ to extend a context $decs$, implicitly dropping the labels in $sdecs$. We define the domain of a structure declaration list, $\text{dom}(sdecs)$, by

$$\text{dom}(lab_1 \triangleright dec_1, \dots, lab_n \triangleright dec_n) = \{lab_1, \dots, lab_n\}.$$

We write $\{var/var'\}L$ for the capture-free substitution of var for free occurrences of var' in L .³

Linkset merge. The rules for linkset merge $decs \vdash L_1 ++ L_2 \rightsquigarrow L_3$ combine L_1 and L_2 to produce L_3 . The rules presuppose that L_1 is well-formed with respect to $decs$ but permit L_2 to make reference (via free TSIL variables) not only to $decs$ but to the imports and exports of L_1 . Formally, the rules satisfy the following property.⁴

If $L_1 = sdecs_1 \rightarrow sbnds : sdecs$
and $decs \vdash L_1 \text{ ok}$
and $decs, sdecs_1, sdecs \vdash L_2 \text{ ok}$,
and $decs \vdash L_1 ++ L_2 \rightsquigarrow L_3$,
then $decs \vdash L_3 \text{ ok}$.

If a linkset is well-formed, then it neither imports nor exports the same label twice (although it may both import and export a particular label).

The rules process the imports in L_2 from left to right. If L_2 has no imports, then the following rule applies.

$$\frac{L = sdecs_0 \rightarrow sbnds : sdecs}{decs \vdash L ++ (\cdot \rightarrow sbnds' : sdecs') \rightsquigarrow sdecs_0 \rightarrow sbnds ++ sbnds' : sdecs ++ sdecs'}$$

L_3 imports what L_1 does and exports what L_1 and L_2 do. To ensure that L_3 is well-formed, the rule uses the TS function $++$ to concatenate the exports in L_1 with the exports in L_2 , renaming labels exported by L_1 that are also exported by L_2 .

Otherwise, the rules examine the first import $lab \triangleright var : sig$ in L_2 and distinguish three mutually exclusive cases:

- L_1 exports lab .

$$\frac{\begin{array}{l} sdecs = sdecs'', lab \triangleright var' : sig', sdecs''' \\ decs, sdecs_0, sdecs \vdash_{\text{sub}} var' : sig' \preceq sig \rightsquigarrow mod : sig'' \\ sbnd := 1 \triangleright var = mod \quad sdec := 1 \triangleright var : sig'' \\ L := sdecs_0 \rightarrow sbnds ++ sbnd : sdecs ++ sdec \\ decs \vdash L ++ (sdecs_1 \rightarrow sbnds' : sdecs') \rightsquigarrow L'' \end{array}}{decs \vdash (sdecs_0 \rightarrow sbnds : sdecs) ++ (lab \triangleright var : sig, sdecs_1 \rightarrow sbnds' : sdecs') \rightsquigarrow L''}$$

³Linkset bound variables and scopes are discussed in Appendix A.

⁴In this description of linkset merge, we suppress all details related to signature abbreviations.

The first premise picks out the L_1 export $lab \triangleright var': sig'$ for lab ; there can be at most one since L_1 is well-formed. The second premise calls the TS coercion compiler to match the export $var': sig'$ to the import signature sig . Linking fails if no match is possible; otherwise, sig'' has the same “shape” as sig , but is fully transparent relative to the variable var' . The structure binding $sbnd : sdec$ is constructed using the coercion module mod at the signature sig'' , maximizing type sharing. The linkset L has the same imports as L_1 , and exports those of L_1 plus the result of the preceding coercion. To ensure that L is well-formed—in particular, that it exports nothing more than once—the rule uses $++$ to construct its exports.

- L_1 imports lab but does not export it.

$$\begin{array}{c}
lab \notin \text{dom}(sdec_s) \\
sdec_s_0 = sdec_s'', lab \triangleright var': sig', sdec_s''' \\
dec_s, sdec_s_0, sdec_s \vdash sig \equiv sig' : \text{Sig} \\
L' := \{var'/var\}(sdec_s_1 \rightarrow sbnds' : sdec_s') \\
dec_s \vdash (sdec_s_0 \rightarrow sbnds : sdec_s) ++ L' \rightsquigarrow L'' \\
\hline
dec_s \vdash (sdec_s_0 \rightarrow sbnds : sdec_s) ++ \\
(lab \triangleright var': sig, sdec_s_1 \rightarrow sbnds' : sdec_s') \rightsquigarrow L''
\end{array}$$

The first premise ensures L_1 does not export lab . The second premise picks out the L_1 import $lab \triangleright var': sig'$. Linking fails if sig and sig' are not equivalent; otherwise, L' is constructed by changing references in the remainder of L_2 to use the import in L_1 .

- L_1 neither imports nor exports lab .

$$\begin{array}{c}
lab \notin \text{dom}(sdec_s) \cup \text{dom}(sdec_s_0) \\
dec_s, sdec_s_0, sdec_s \vdash sig \equiv sig' : \text{Sig} \\
dec_s, sdec_s_0 \vdash sig' : \text{Sig} \\
L := sdec_s_0, lab \triangleright var': sig' \rightarrow sbnds : sdec_s \\
dec_s \vdash L ++ (sdec_s_1 \rightarrow sbnds' : sdec_s') \rightsquigarrow L'' \\
\hline
dec_s \vdash (sdec_s_0 \rightarrow sbnds : sdec_s) ++ \\
(lab \triangleright var': sig, sdec_s_1 \rightarrow sbnds' : sdec_s') \rightsquigarrow L''
\end{array}$$

The first premise ensures that L_1 neither imports nor exports lab . The next two premises choose a signature sig' equivalent to sig but well-formed without reference to the exports of L_1 . Linking fails if no such signature exists—when opaque types exported by L_1 occur in sig . Otherwise, L is constructed by adding a new import to the imports in L_1 .

3.2 Elaboration

We define a semantics for SMLSC by giving rules for the elaboration judgements in Figure 6. We give the abstract syntax for SMLSC in Figure 7. The elaboration rules appear in Appendix B. These judgements have the following meaning.

- $project \rightsquigarrow L$. Elaborate $project$, using linkset merge to accumulate a resulting linkset L . A source unit is elaborated in a context Γ that declares the imports and exports in L .
- $\Gamma \vdash srcunit \rightsquigarrow L$. Elaborate the $topdec$ in $srcunit$ to the linkset

$$sdec_s_0 \rightarrow sbnds : sdec_s; S.$$

<i>Judgement...</i>	<i>Meaning...</i>
$project \rightsquigarrow L$	project elaboration
$\Gamma \vdash srcunit \rightsquigarrow L$	unit elaboration
$\Gamma \vdash topdec \rightsquigarrow L$	top-level declaration elaboration
$\Gamma \vdash impexp \rightsquigarrow L$	import expression elaboration
$\Gamma \vdash sigbind \rightsquigarrow S$	signature binding elaboration
$\Gamma \vdash_{ctx} sigid \rightsquigarrow sig : Sig$	signature lookup
$\Gamma \vdash_{ctx} unitid \rightsquigarrow S$	
$\Gamma \text{ ok}$	Γ is well-formed

Figure 6: Elaboration judgements

$project$	$::=$	\cdot	empty
		$project, srcunit$	source unit
		$project, L$	compiled unit(s)
$srcunit$	$::=$	unit $unitid = topdec$	unit declaration
$topdec$	$::=$	import $impexp$	open units
		$strdec$	
		signature $sigbind$	
		local $topdec_1$ in $topdec_2$ end	
		$topdec_1 topdec_2$	
$impexp$	$::=$	$unitid \langle : \text{intf spec end} \rangle$	open $unitid$
		$impexp_1 impexp_2$	
$sigbind$	$::=$	$sigid = sigexp \langle \text{and } sigbind \rangle$	

Figure 7: SMLSC abstract syntax

The imports $sdecs_0$ arise from the import declarations in $topdec$. The exports $sbnds : sdecs$ arise from the structure declarations in $topdec$. The signature abbreviations S arise from the signature declarations in $topdec$. The result, L , exports a single module

$$\overline{unitid} \triangleright var = [sbnds] : \overline{unitid} \triangleright var : [sdecs].$$

- $\Gamma \vdash topdec \rightsquigarrow L$. Elaborate $topdec$ using linkset merge and identifier resolution.
- $\Gamma \vdash impexp \rightsquigarrow L$. Elaborate $impexp$ using identifier resolution and $spec$ elaboration.
- $\Gamma \vdash sigbind \rightsquigarrow S$. Elaborate $sigbind$ using signature elaboration.

4 Semantics extending *The Definition of Standard ML*

In this section, we give a semantics to SMLSC by extending *The Definition of Standard ML* (TD) [16]. TD gives a semantics to SML by relating it to *semantic objects*—mathematical sets, functions, and so on. We refer to these semantic objects collectively as the *internal language* (TDIL) and to SML as the *external language* (TDEL). The TDIL is partitioned into *static* and *dynamic* semantic objects. TD’s static semantics specifies type checking and type inference using the static TDIL. TD’s dynamic semantics gives the TDEL a big-step, call-by-value operational semantics using the dynamic TDIL.

A unit declaration list $udecs$ is a list of source units $srcunit_1, \dots, srcunit_n$ (see Figure 8). In Section 4.1 we review TD’s static semantics and extend it to unit declaration lists. In Section 4.2 we do the same for TD’s dynamic semantics. We define linksets and linking in Section 4.3. A linkset contains source code—a unit declaration list—and static TDIL: Separate compilation corresponds to separate type checking. In Section 4.4 we give a semantics to SMLSC through an elaboration into linksets.

4.1 Static Semantics

We shall use the TD static semantic judgements given in Figure 9. These judgements have the following meaning.

- $B \vdash strdec \Rightarrow E$. The structure declaration $strdec$ is well-typed and declares the structure, type, and value identifiers in environment E .
- $B \vdash sigdec \Rightarrow G$. The signature declaration $sigdec$ is well-formed and declares the signature identifiers in signature environment G .
- $B \vdash fundec \Rightarrow F$. The functor declaration $fundec$ is well-typed and declares the functor identifiers in functor environment F .
- $B \vdash sigexp \Rightarrow \Sigma$. The signature expression $sigexp$ is well-formed and specifies the components in signature Σ .

$$udecs ::= \cdot \quad \text{empty} \\ udec, srcunit \quad \text{unit declaration}$$

Figure 8: Unit syntax

<i>Judgement...</i>	<i>Meaning...</i>
$B \vdash \text{strdec} \Rightarrow E$	structure declaration elaboration
$B \vdash \text{sigdec} \Rightarrow G$	signature declaration elaboration
$B \vdash \text{fundec} \Rightarrow F$	functor declaration elaboration
$B \vdash \text{sigexp} \Rightarrow \Sigma$	signature expression elaboration
$B \vdash \text{spec} \Rightarrow E$	specification elaboration
$\Sigma \geq E \text{ using } \varphi$	signature instantiation
$E_1 \succ E_2$	enrichment

Figure 9: TD’s static semantic judgements (summary)

- $B \vdash \text{spec} \Rightarrow E$. The specification *spec* is well-formed and specifies the components in *E*.
- $\Sigma \geq E \text{ using } \varphi$. The environment *E* is an instance of the signature Σ using the realization φ .
- $E_1 \succ E_2$. The environment *E*₁ may have more components than *E*₂, it may be less polymorphic, and it may change the status of constructors/exceptions to values.

One subtlety in these judgements pervades our semantics: They account for TDEL type sharing by stamping TDIL types with *type names* and TDEL type generativity by using state-passing to track the set of type names that “have been generated”. Two TDIL types share if they are stamped with the same type name. To see how state-passing works, consider the judgement

$$B \vdash \text{strdec} \Rightarrow E.$$

The basis $B = T, F, G, E'$ comprises a context and a state. The context F, G, E' assigns static TDIL to those identifiers that may occur free in *strdec*. The state *T* is a set of type names. Rules that generate types (e.g., the rule for **datatype** declarations) choose type names not in *T*: Types stamped with such names do not share with any types in *B*.

TDEL signature matching complicates the tracking of type names. The rule for opaque signature ascription $\text{strex}p \rightarrow \text{sigexp}$ generates types after elaborating *strex**p* and *sigexp*. Consider the following structure declaration.

```

structure A = struct type a = int type b = a end
               :> sig    type a          type b = a end

```

Types *A.a* and *A.b* share but neither shares with *int*. At the level of the TDIL, *A.a* must be stamped with a new type name and *A.b* must be stamped with the *same* type name. To handle the book-keeping, a TDIL signature $\Sigma = (T)E$ comprises a set *T* of bound type names (induced by abstract type specifications) and an environment *E* describing its components. The example elaborates as follows.

1. The inner structure expression “**struct type a = int type b = a end**” elaborates to an environment *E* mapping type constructors *a* and *b* to types stamped with t_{int} (where t_{int} is the type name associated with *int* in the ambient basis).
2. The signature expression elaborates to a signature $\Sigma = (T)E'$ where $T = \{t\}$ binds one type name and *E'* maps the type constructors *a* and *b* to types stamped with *t*.

$uspecs$	\in	$\text{UnitSpecs} = \bigcup_{n \geq 0} \text{UnitSpecs}^n$
$unitid:\Upsilon_1, \dots, unitid:\Upsilon_n$	\in	$\text{UnitSpecs}^n = (\text{UnitId} \times \text{UnitSig})^n$
Υ or $(T)(F, G, E)$	\in	$\text{UnitSig} = \text{TyNameSet} \times (\text{FunEnv} \times \text{SigEnv} \times \text{Env})$
Γ or B, U	\in	$\text{UnitBasis} = \text{Basis} \times \text{UnitEnv}$
U	\in	$\text{UnitEnv} = \text{UnitId} \xrightarrow{\text{fin}} \text{Basis}$
$unitid$	\in	UnitId (unit identifiers)

Figure 10: Static TDIL for unit declaration lists

3. The opaque ascription (a structure expression) elaborates to E' after the bound type name in Σ is systematically renamed so that it differs from all type names in the ambient basis (e.g., $t \neq t_{\text{int}}$).
4. The structure declaration elaborates to an environment E_A mapping the structure identifier A to the environment E' obtained in (3). Thus the types $A.a$ and $A.b$ in E_A are stamped with a fresh type name as required.

The rule for transparent signature ascription $strexp : sigexp$ induces sharing after elaborating $strexp$ and $sigexp$. Consider the following structure declaration.

```

structure B = struct type a = int type b = a end
              : sig           type b      end

```

Types $B.b$ and int share. To induce sharing, TD uses capture-avoiding substitution from type names to types: The rule chooses and applies a *realization* φ . This example elaborates as follows.

1. The inner structure expression elaborates as in the preceding example.
2. The signature expression elaborates to a signature $\Sigma' = (T')E''$ where $T' = \{t'\}$ binds one type name and E'' maps the type constructor b to a type stamped with t' .
3. The transparent ascription elaborates to $\varphi(E'')$ where $\varphi(\cdot)$ can be applied to any semantic object A to substitute t_{int} (the type name associated with b in E) for free occurrences of t' in A .
4. The structure declaration elaborates to an environment E_B mapping the structure identifier B to the environment $\varphi(E'')$. Thus the type $B.a$ in E_B is stamped with t_{int} as required.

Both ascription rules employ the signature instantiation and enrichment relations to define TDEL signature matching in terms of TDIL environments and signatures.

Unit Static Semantics In Figure 10 we extend the static TDIL for unit declaration lists. These TDIL categories—disjoint from all others—build on TD’s categories TyNameSet , FunEnv , SigEnv , Env , and Basis (see Appendix C). When specifying TDIL we use the following notation.

- $A \times B$ denotes the cartesian product of A and B .
- $A \cup B$ denotes the disjoint union of A and B .
- $A \xrightarrow{\text{fin}} B$ denotes the set of partial functions from A to B with finite domain.

<i>Judgement...</i>	<i>Meaning...</i>
$\Gamma \vdash udec s : uspec s$	<i>udecs</i> has specification list <i>uspecs</i>
$\Gamma \vdash topdec : B$	<i>topdec</i> has basis <i>B</i>
$\Gamma \vdash impexp : F, G, E$	<i>impexp</i> has components <i>F, G, E</i>
$B \vdash interp \Rightarrow \Upsilon$	interface expression elaboration
$B \vdash topspec \Rightarrow F, E$	top-level specification elaboration
$B \vdash funspec \Rightarrow F$	functor specification elaboration
$\Gamma \text{ ok}$	Γ is well-formed
$\Gamma \vdash \Upsilon : \text{Sig}$	Υ is well-formed
$\Gamma \vdash uspec s \text{ ok}$	<i>uspecs</i> is well-formed
$E : \Sigma \text{ using } \varphi$	signature matching
$\Sigma \equiv \Sigma' \text{ using } \varphi$	signature equivalence
$\Upsilon \equiv \Upsilon' \text{ using } \varphi$	interface equivalence
$F, G, E : \Upsilon \text{ using } \varphi$	interface matching

Figure 11: Unit static semantics

- A^n denotes a sequence of length $n \geq 0$ whose range is a subset of A .

A unit signature (or *interface*) $\Upsilon = (T)(F, G, E)$ describes a unit. Υ specifies the components in environments F , G , and E , binding type names T with scope (F, G, E) .

A unit specification list $unitid_1:\Upsilon_1, \dots, unitid_n:\Upsilon_n$ describes a unit declaration list. Writing $\text{BT}(\Upsilon)$ for the type names bound in Υ , a unit specification list binds $\text{BT}(\Upsilon_i)$ with scope $unitid_{i+1}:\Upsilon_{i+1}, \dots, unitid_n:\Upsilon_n$ for each $1 \leq i < n$.

A unit basis $\Gamma = B, U$ (where $B = T, F, G, E$) comprises a state T and a context F, G, E, U . The unit environment U is a finite map from unit identifiers to bases: If $U(unitid) = T', F', G', E'$, then $T' \subset T$ records the type names generated by *unitid* and the environments F', G', E' describe its components.

We give a static semantics to unit declaration lists by giving rules for the judgements in Figure 11. The rules appear in Appendix C. These judgements have the following meaning.

- $\Gamma \vdash udec s : uspec s$. The unit declaration list *udecs* matches the unit specification list *uspecs*. Corresponding unit identifiers must agree and each source unit in *udecs* must match its specification in *uspecs*. For example, the judgement

$$\Gamma \vdash (\text{unit } unitid = \text{unit } topdec \text{ end}, udec s) : (unitid:(T)(F, G, E), uspec s)$$

holds if $\Gamma \vdash topdec : T, F, G, E$ and $\Gamma + T + \{unitid \mapsto T, F, G, E\} \vdash udec s : uspec s$.⁵

- $\Gamma \vdash topdec : B$. The top-level declaration *topdec* has basis $B = T, F, G, E$: It generates type names T and declares the components in F, G, E .
- $\Gamma \vdash impexp : F, G, E$. The import expression *impexp* imports the components in F, G, E from Γ .

⁵The notation $\Gamma + T + \{unitid \mapsto T, F, G, E\}$ extends the state then the context in Γ (see Appendix C).

- $B \vdash \text{intexp} \Rightarrow \Upsilon$. The interface expression *intexp* specifies the components in Υ . Since interface expressions do not describe signature declarations, the signature environment G in Υ must be empty.
- $B \vdash \text{topspec} \Rightarrow F, E$. The top-level specification *topspec* specifies the components in F, E . No identifier may be specified twice.
- $B \vdash \text{funspec} \Rightarrow F$. The functor specification *funspec* specifies the components in F . No identifier may be specified twice.
- $\Gamma \text{ ok}$. The unit basis $\Gamma = (T, F, G, E), U$ is well-formed: T contains the free type names in F, G, E, U .
- $\Gamma \vdash \Upsilon : \text{Sig}$. The interface $\Upsilon = (T)(F, G, E)$ is well-formed: No type name $t \in T$ occurs in Γ 's state T' and $T \cup T'$ contains the free type names in F, G, E .
- $\Gamma \vdash \text{uspecs ok}$. The unit specification list *uspecs* is well-formed. For example, the judgement

$$\Gamma \vdash \text{unitid}:\Upsilon, \text{uspecs ok}$$

holds if $\Gamma \vdash \Upsilon : \text{Sig}$ and $\Gamma + \text{BT}(\Upsilon) \vdash \text{uspecs ok}$.

- $E : \Sigma \text{ using } \varphi$. The environment E matches the signature Σ using the realization φ .
- $\Sigma \equiv \Sigma' \text{ using } \varphi$. The signatures $\Sigma = (T)E$ and $\Sigma' = (T')E'$ specify the same components and $E = \varphi(E')$.
- $\Upsilon \equiv \Upsilon' \text{ using } \varphi$. The interfaces $\Upsilon = (T)(F, G, E)$ and $\Upsilon' = (T')(F', G', E')$ specify the same components and $F, G, E = \varphi(F', G', E')$.
- $F, G, E : \Upsilon \text{ using } \varphi$. The environments F, G, E match the interface Υ and the realization φ induces the requisite sharing: If the semantic object A refers to type names bound in Υ , then the semantic object $\varphi(A)$ refers to corresponding types employed by F, G, E .

4.2 Dynamic Semantics

We shall use the TD dynamic semantic judgements given in Figure 9.⁶ These judgements have the following meaning.

⁶In many cases, static and dynamic TDIL categories have the same names and employ the same metavariables. No confusion can result since the static and dynamic semantics are separate.

<i>Judgement...</i>	<i>Meaning...</i>
$s, B \vdash \text{strdec} \Rightarrow E, s'$	structure declaration evaluation
$B \vdash \text{fundec} \Rightarrow F$	functor declaration evaluation
$IB \vdash \text{sigdec} \Rightarrow G$	signature declaration elaboration
$IB \vdash \text{sigexp} \Rightarrow I$	signature elaboration
$IB \vdash \text{spec} \Rightarrow I$	specification elaboration
Inter $B = IB$	interface basis extraction
$E \downarrow I = E'$	signature ascription

Figure 12: TD's dynamic semantic judgements (summary)

- $s, B \vdash \text{strdec} \Rightarrow E/p, s'$. In state s and context B , the structure declaration strdec evaluates to state s' and either an environment E or an exception packet p .
- $B \vdash \text{fundec} \Rightarrow F$. The functor declaration fundec evaluates to the functor environment F . State is not involved: F maps functor identifiers to functor closures.
- $IB \vdash \text{sigdec} \Rightarrow G$. The signature declaration sigdec declares the signature identifiers in signature environment G .
- $IB \vdash \text{sigexp} \Rightarrow I$. The signature expression sigexp specifies the components in structure interface I .
- $IB \vdash \text{spec} \Rightarrow I$. The specification spec specifies the components in I .
- $\text{Inter } B = IB$. The interface basis IB is the value-free part of the basis B . (Values are not needed by the signature elaboration judgements.)
- $E \downarrow I = E'$. The environment E' is the environment E cut down to match the structure interface I .

The dynamic semantics is defined for mostly type-erased TDEL. Types, type ascriptions, and type qualifications are erased. Signatures are *not* erased. Both signature ascription and functor application limit the “view” of a structure in case it is opened. Consider the following declarations.

```

structure A = struct val x = 1    val y = 2 end
                : sig    val x : int                end
val y = 3
open A

```

The value of y is 3 not 2. A related example employs functors:

```

functor F(S : sig val x : int end) =
  struct
    val y = 3
    open S
  end
structure B = F(struct val x = 1 val y = 2 end)

```

The value of $B.y$ is 3 not 2. The dynamic semantics uses structure interfaces to cut down environments when evaluating signature ascriptions and functor applications. To obtain structure interfaces, the dynamic semantics re-elaborates signatures. This “dynamic elaboration” tracks only the status of identifiers, making it simpler than the elaboration performed by the static semantics. For example, it is stateless.

In the evaluation judgement

$$s, B \vdash \text{strdec} \Rightarrow E/p, s',$$

the states s and s' track a set of generated *exception names* and a memory graph for references. The dynamic semantics accounts for the generativity of exception bindings by stamping exception values with exception names—two TDIL exceptions are equal if they are stamped with the same name. The rules propagate raised exceptions explicitly, referring to them as exception packets p . (Compound metavariables like E/p range over the disjoint union of two TDIL categories.)

$$\begin{aligned}
UI \text{ or } FI, I &\in \text{UnitInt} = \text{FunIntEnv} \times \text{Int} \\
FI &\in \text{FunIntEnv} = \text{FunId} \xrightarrow{\text{fin}} \text{Int} \times \text{Int} \\
\Gamma &\in \text{UnitBasis} = \text{Basis} \times \text{UnitEnv} \\
U &\in \text{UnitEnv} = \text{UnitId} \xrightarrow{\text{fin}} \text{Basis}
\end{aligned}$$

Figure 13: Unit dynamic semantic objects

Unit Dynamic Semantics. In Figure 13 we extend the dynamic TDIL for unit declaration lists. These TDIL categories—disjoint from all others—build on TD’s dynamic TDIL categories Basis and Int (see Appendix D).

As with TDEL signatures, interface expressions are not erased prior to evaluation. An SC import declaration `import unitid : interxp` is analagous to a TDEL signature ascription and an open declaration: It limits the “view” of the imported unit. We shall give a dynamic semantics that uses (dynamic) unit interfaces to cut down bases when elaborating SC imports.

A unit interface $UI = FI, I$ describes a unit. The structure interface I describes structure, type, and value components. The functor interface environment FI describes functor components using functor interfaces. A functor interface I, I' comprises argument and result structure interfaces. Both are necessary. Consider the following unit declaration list.

```

unit U1 =
unit
  functor F(S : sig end) = struct open S val x = 1 val y = 2 end
end,

unit U2 =
unit
  import U1 :
  intf
    functor F(S : sig val x : int end) : sig val x : int end
  end
  structure A = F(struct val x = 3 end)
  val y = 4
  open A
end

```

The values of x and y in $U2$ are, respectively, 1 and 4 rather than 3 and 2.

A unit basis $\Gamma = B, U$ serves as an evaluation context. The unit environment U is a finite map from unit identifiers to bases: If $U(\text{unitid}) = F, G, E$, then the environments F, G, E record the values obtained by evaluating *unitid*.

We give a dynamic semantics to unit declaration lists by giving rules for the evaluation judgements in Figure 14. The rules appear in Appendix D. These judgements have the following meaning.

- $s, \Gamma \vdash \text{udecs} \Rightarrow \Gamma'/p, s'$. Evaluate the unit declaration list *udecs* to a unit basis Γ' or an exception packet p .
- $s, \Gamma \vdash \text{topdec} \Rightarrow B/p, s'$. Evaluate the top-level declaration *topdec* to a basis B or an exception packet p .

<i>Judgement...</i>	<i>Meaning...</i>
$s, \Gamma \vdash udec \Rightarrow \Gamma'/p, s'$	unit evaluation
$s, \Gamma \vdash topdec \Rightarrow B/p, s'$	top-level declaration evaluation
$\Gamma \vdash impexp \Rightarrow B$	import expression evaluation
$IB \vdash topspec \Rightarrow UI$	top-level specification elaboration
$IB \vdash funspec \Rightarrow FI$	functor specification elaboration

Figure 14: Unit evaluation judgements

- $\Gamma \vdash impexp \Rightarrow B$. Evaluate the import expression *impexp* to the basis *B*.
- $IB \vdash topspec \Rightarrow UI$. The top-level specification *topspec* specifies the components in the unit interface *UI*.
- $IB \vdash funspec \Rightarrow FI$. The functor specification *funspec* specifies the components in the functor interface environment *FI*.

4.3 Linking

We define linking for SMLSC by giving rules for deriving the judgements in Figure 15. A linkset

$$uspecs_0 \rightarrow exps$$

comprises imports *uspecs*₀ and exports *exps*. Exports may take two forms—a unit declaration list *udecs* : *uspecs* or a static TDIL basis *B*.

- The imports *uspecs*₀ describe the units on which the linkset depends; they must be well-formed in the ambient context and no unit may be described twice. For example, the imports

$$uspecs_{AB} = A:\Upsilon_A, B:\Upsilon_B$$

express dependency on units *A* and *B*.

Imports specify assumptions to be satisfied by linking. A linkset with imports *uspecs*_{AB} assumes unit *B* declares (at least) the components described by the interface Υ_B but can be linked with (a linkset exporting) a unit *B* providing more components.

- The exports *exps* = *udecs* : *uspecs* are the code associated with the linkset. They may make reference to the linkset's imports (via free unit identifiers and type names).

<i>Judgement...</i>	<i>Meaning...</i>
$\Gamma \vdash L \text{ ok}$	<i>L</i> is well-formed
$\Gamma \vdash exps \text{ ok}$	<i>exps</i> is well-formed
$L \Rightarrow udec$	<i>L</i> completes to <i>udecs</i>
$\Gamma \vdash L ++ L' \Rightarrow L''$	<i>L</i> and <i>L'</i> merge to <i>L''</i>
$\Gamma \vdash exps ++ exps' \Rightarrow exps''$	<i>exps</i> and <i>exps'</i> merge to <i>exps''</i>

Figure 15: Linking judgements

L	$::=$	$uspecs \rightarrow exps$	linkset
$exps$	$::=$	$udecs : uspecs$	units
		B	top-level components

Figure 16: Linkset syntax

- The exports $exps = B$ arise during elaboration and record typing information for the top-level declaration associated with the linkset. They may make reference to the linkset's imports (via free type names).

The dynamic semantics for SMLSC is very simple. The completion judgment $L \Rightarrow udec$ translates a linkset

$$\cdot \rightarrow udec : uspecs$$

with no imports to the unit declaration list $udecs$. Under the dynamic semantics for units given in Section 4.2, the resulting unit declaration list evaluates the linkset's exports from left to right for their side-effects.⁷ Evaluation terminates when an uncaught exception is raised or when every export has been evaluated.

We give the full syntax for linksets in Figure 16 and the rules in Appendix E. The remainder of this section explains the rules for linkset merge.

Notation. We define the extension of a unit basis by a unit specification list, $\Gamma + uspecs$, by⁸

$$\begin{aligned} \Gamma + \cdot &= \Gamma \\ \Gamma + (unitid:(T)(F, G, E), uspecs) &= (\Gamma + T + \{unitid \mapsto T, F, G, E\}) + uspecs. \end{aligned}$$

We define the type names bound by a unit specification list, $BT(uspecs)$, by

$$BT(unitid_1:\Upsilon_1, \dots, unitid_n:\Upsilon_n) = BT(\Upsilon_1) \cup \dots \cup BT(\Upsilon_n).$$

We define the domain of a unit specification list, $dom(uspecs)$, by

$$dom(unitid_1:\Upsilon_1, \dots, unitid_n:\Upsilon_n) = \{unitid_1, \dots, unitid_n\}$$

and the domain of a linkset's exports, $dom(exps)$, by

$$\begin{aligned} dom(udecs : uspecs) &= dom(uspecs) \\ dom(B) &= \emptyset. \end{aligned}$$

Linkset merge. The rules for linkset merge $\Gamma \vdash L_1 ++ L_2 \Rightarrow L_3$ combine L_1 and L_2 to produce L_3 . The rules presuppose that L_1 is well-formed with respect to Γ but permit L_2 to make reference not only to Γ but to the imports and exports of L_1 .

The rules process the imports in L_2 from left to right. If L_2 has no imports, then the following rule applies.

$$\frac{\Gamma \vdash exps ++ exps' \Rightarrow exps''}{\Gamma \vdash (uspecs_0 \rightarrow exps) ++ (\cdot \rightarrow exps') \Rightarrow uspecs_0 \rightarrow exps''}$$

⁷The unit declaration list $udecs$ obtained by completion may be evaluated in the initial state s_0 and the unit basis $\Gamma_0 = B_0, \{\}$, where s_0 and the initial dynamic basis B_0 are given in TD [16, Appendix D].

⁸Please see Appendix C for a summary of TDIL notation, including definitions of $\Gamma + T$, $\Gamma + U$, and $\Gamma(unitid)$.

L_3 imports what L_1 does. The rules for $\Gamma \vdash \text{exprs} ++ \text{exprs}' \Rightarrow \text{exprs}''$ ensure that L_3 exports what L_1 and L_2 do.

Otherwise, the rules examine the first import $\text{unitid}:\Upsilon$ in L_2 and distinguish three mutually exclusive cases:

- L_1 exports unitid .

$$\frac{\begin{array}{c} \text{unitid} \in \text{dom}(\text{uspecs}) \\ (\Gamma + \text{uspecs}_0 + \text{uspecs})(\text{unitid}) = T', F', G', E' \\ F', G', E' : \Upsilon \text{ using } \varphi \\ L' := \varphi(\text{uspecs}_1 \rightarrow \text{udecs}' : \text{uspecs}') \\ \Gamma \vdash (\text{uspecs}_0 \rightarrow \text{udecs} : \text{uspecs}) ++ L' \Rightarrow L'' \end{array}}{\Gamma \vdash (\text{uspecs}_0 \rightarrow \text{udecs} : \text{uspecs}) ++ (\text{unitid}:\Upsilon, \text{uspecs}_1 \rightarrow \text{udecs}' : \text{uspecs}') \Rightarrow L''}$$

The first premise ensures L_1 exports unitid . The second premise picks out the L_1 export $\text{unitid} : (T')(F', G', E')$ for unitid . The third premise matches the exported environments F', G', E' to the imported interface Υ . Linking fails if no match is possible; otherwise, L' is constructed by changing references to the type names bound by Υ in the remainder of L_2 to the types employed by L_1 .

- L_1 imports unitid but does not export it.

$$\frac{\begin{array}{c} \text{unitid} \notin \text{dom}(\text{exprs}) \\ \text{uspecs}_0 = \text{uspecs}'', \text{unitid}:\Upsilon', \text{uspecs}''' \\ \Upsilon' \equiv \Upsilon \text{ using } \varphi \\ L' := \varphi(\text{uspecs}_1 \rightarrow \text{exprs}') \\ \Gamma \vdash (\text{uspecs}_0 \rightarrow \text{exprs}) ++ L' \Rightarrow L'' \end{array}}{\Gamma \vdash (\text{uspecs}_0 \rightarrow \text{exprs}) ++ (\text{unitid}:\Upsilon, \text{uspecs}_1 \rightarrow \text{exprs}') \Rightarrow L''}$$

The first premise ensures L_1 does not export unitid . The second premise picks out the L_1 import $\text{unitid}:\Upsilon'$; there can be at most once since L_1 is well-formed. Linking fails if Υ and Υ' are not equivalent; otherwise, L' is constructed by changing references to the type names bound by Υ in the remainder of L_2 to the type names bound by Υ' .

- L_1 neither imports nor exports unitid .

$$\frac{\begin{array}{c} \text{unitid} \notin \text{dom}(\text{exprs}) \cup \text{dom}(\text{uspecs}_0) \\ \Upsilon' \equiv \Upsilon \text{ using } \varphi \\ \Gamma + \text{BT}(\text{uspecs}_0) \vdash \Upsilon' : \text{Sig} \\ L := \text{uspecs}_0, \text{unitid}:\Upsilon' \rightarrow \text{exprs} \\ L' := \varphi(\text{uspecs}_1 \rightarrow \text{exprs}') \\ \Gamma \vdash L ++ L' \Rightarrow L'' \end{array}}{\Gamma \vdash (\text{uspecs}_0 \rightarrow \text{exprs}) ++ (\text{unitid}:\Upsilon, \text{uspecs}_1 \rightarrow \text{exprs}') \Rightarrow L''}$$

The first premise ensures that L_1 neither imports nor exports unitid . The next two premises choose an interface Υ' equivalent to Υ but well-formed without reference to the exports of L_1 . Linking fails if no such interface exists—when type names exported by L_1 occur in Υ . Otherwise, L is constructed by adding a new import to the imports in L_1 and L' is constructed by changing references to the type names bound in Υ in the remainder of L_2 to the type names bound in Υ' .

<i>Judgement...</i>	<i>Meaning...</i>
$project \Rightarrow L$	project elaboration
$\Gamma \vdash srcunit \Rightarrow L$	unit elaboration
$\Gamma \vdash topdec \Rightarrow L$	top-level declaration elaboration
$\Gamma \vdash impexp \Rightarrow L$	import expression elaboration

Figure 17: Elaboration judgements

4.4 Elaboration

We define a semantics for SMLSC by giving rules for the elaboration judgements in Figure 17. We give the abstract syntax for SMLSC in Figure 18. The elaboration rules appear in Appendix F. These judgements have the following meaning.

- $project \Rightarrow L$. Elaborate *project*, using linkset merge to accumulate a resulting linkset *L*. A source unit is elaborated in a unit basis Γ that declares the imports and exports in *L*.
- $\Gamma \vdash srcunit \Rightarrow L$. Elaborate the *topdec* in *srcunit* to the linkset

$$udecs_0 \rightarrow T, F, G, E.$$

The imports $udecs_0$ arise from the import declarations in *topdec*. The type names *T* arise from the types generated by *topdec*. The environments *F*, *G*, and *E* arise from the declarations in *topdec*. The result, *L*, exports a single unit:

$$L = udec_0 \rightarrow srcunit : (T)(F, G, E).$$

- $\Gamma \vdash topdec \Rightarrow L$. Elaborate *topdec* using linkset merge.
- $\Gamma \vdash impexp \Rightarrow L$. Elaborate *impexp* using context lookup and *intexp* elaboration.

5 Implementation

The semantics of SMLSC avoids commitment to the meaning of “compilation,” “linking,” and “completion” to ensure compatibility with various implementation strategies. These phases may be implemented using classical methods (code generation during compilation, object code weaving during linking, and writing an executable for completion), or in other, more novel, ways (such as type checking during compilation, and code generation during linking). The design is, as far as we know, implementable in all current Standard ML compilers without requiring radical changes to their infrastructure.

<i>project</i>	$::=$	\cdot	empty
		<i>project, srcunit</i>	source unit
		<i>project, L</i>	compiled unit(s)

Figure 18: SMLSC abstract syntax

Parallel Build. A compiler can exploit interfaces to support parallel compilation in order to speed up system build times. A unit can be compiled once interfaces have been inferred for its IC imports. The TILT compiler, which implements an earlier version of the present extension, implements such a strategy. Moreover, it also implements cut-off incremental recompilation [1], where it is able to interrupt the normal cascade of recompilation when a source change does not cause a unit’s interface to change.

Parsing. This presentation of SMLSC provides concrete and abstract syntax, but does not formalize parsing. The only issue that entangles separate compilation and parsing is fixity declarations. To support fixity declarations at parse-time, we include a parsing context in the concrete representation of linksets (object files). A source unit that is incrementally compiled against a linkset is parsed using that linkset’s included parsing context. We do not permit fixity specifications in user-specified interfaces, and therefore they do not affect interface matching or any other part of the semantics.

Note that a library may specify fixity information by placing appropriate declarations in the handoff unit. For example, to describe a matrix library that supplies an infix `**` operator for multiplication, we may write the following handoff unit:

```
unit Matrices =
unit
  import MatricesImpl :
  intf
    type matrix
    val ** : matrix * matrix -> matrix
    (* ... *)
  end
  infix **
end
```

6 Multiple Interfaces for the Same Import

In Section 2 we presented the programming methodology of handoff units. As long as two linksets that import the same unit identifier do so by using the same handoff unit, they will always agree on the interface for that unit and so can be linked together. However, in some situations it may be useful to permit two clients to import the same unit, each with a different interface. Since interface matching, like signature matching, is coercive, this complicates the methodology of definite references by introducing “views” of the same underlying unit.

For example, suppose that two linksets L_1 and L_2 import the same unit `MathLib` at disparate interfaces I_1 and I_2 . This may happen because the developers of L_1 and L_2 compiled using different versions of the handoff unit for `MathLib`, or because the developers wrote their import interfaces by hand. The link

$$link(L_1, L_2)$$

fails because the linksets are required to agree on the interfaces of their common imports. Aside from recompiling the two linksets to use the same interface, the programmer has several options for resolving this situation. First, she can satisfy the imports by providing the implementation of

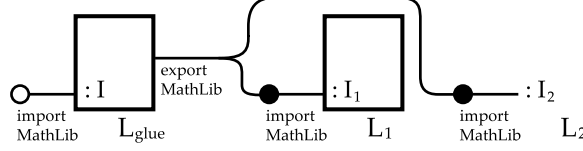


Figure 19: The linkset L_{glue} imports **MathLib** at interface I and then exports it to satisfy the imports in L_1 and L_2 at disparate interfaces I_1 and I_2 .

MathLib:

$$\begin{aligned} L'_1 &= \text{link}(\text{MathLib}, L_1) \\ L &= \text{link}(L'_1, L_2) \end{aligned}$$

The first step satisfies the SC import of **MathLib** in L_1 , as long as the actual interface of **MathLib** matches the import interface I_1 . The result L'_1 does not import **MathLib**, so it does not conflict with the import of **MathLib** in L_2 . L'_1 does export **MathLib**, so if the actual interface of **MathLib** matches I_2 , then the second link succeeds. Because linking is left-associative, $L = \text{link}(\text{MathLib}, L_1, L_2)$ accomplishes the same thing.

Any implementation of **MathLib** that satisfies both I_1 and I_2 will suffice. Because we do not require unit names to be globally unique, this implementation of **MathLib** might even import **MathLib** (again) and then contain some glue code to make it compatible with the two given interfaces I_1 and I_2 (Figure 19). We expect such cases to be uncommon, the preferred methodology being to use a single handoff unit for all clients.

7 Related Work

There are several closely related systems that influenced the design of SMLSC.

The notion of linkset in SMLSC comes from Cardelli's investigation of separate compilation and type-safe linking in the simply-typed λ -calculus [5]. Our formalization of linking extends these ideas to support the Standard ML module system including signature subtyping, abstract types, and module and type definitions in structures.

Harper and Pierce [11] discuss language design for module systems, including separate compilation. Particularly relevant to the current work is their discussion of sharing of abstract types. They describe the use of definite references to avoid the coherence problems (and excess sharing specifications) that arise from aliasing.

The notion of a handoff unit bears some resemblance to the use of `.h` files in C. The presence of function prototypes in a `.h` file provides an interface for application code that includes that header file. Code that references a prototyped function triggers a link-time demand for that function. The degree of link-time type-checking varies across C implementations. Usually, type correctness is assured by programming conventions.

Glew and Morrisett [8] describe separate compilation for Typed Assembly Language [19]. Their language, MTAL, permits type definitions, abstract types, and polymorphic types in interfaces and supports recursive linking.

Jim [14] describes a λ -calculus \mathbf{P}_2 with rank 2 intersection types that has *principal typings*. The principal typings property means that from a term M , one can infer both Γ and τ such that any typing derivation $\Gamma' \vdash M : \tau'$ is an instance of $\Gamma \vdash M : \tau$. In a system with principal typings, program fragments can be separately compiled without context information, meaning that SC imports need not even specify interfaces. Standard ML, however, does not have principal typings.

It remains an open problem to design a type system that supports principal types for features such as abstract and recursive types, and type definitions in modules.

Objective Caml. The separate compilation system of Objective Caml (O’Caml) [15] is similar in many regards to SMLSC. The declaration of a unit U is an O’Caml module stored within a file called $U.ml$. The interface for U may optionally be given in a file called $U.mli$. If the interface is present, other units depending on U can compile even if the implementation is not available, just as in SMLSC. Because the filename of an interface indicates the unit that it describes, O’Caml interfaces play the role of handoff units in SMLSC. Additionally, O’Caml’s use of the filesystem to provide a canonical location for each unit and interface means that all unit references are definite.

On the other hand, O’Caml’s dependence on the filesystem means that the language is not independent from its environment. For instance, unit names are limited to valid filenames on the host system, and restructuring a project on disk may force changes to the code. Another significant difference is that O’Caml conflates the notions of units and modules. This earns O’Caml some conceptual economy, but it makes it impossible to separate the notions of top-level declarations and structure components. This makes it necessary to support signature and functor definitions within structures, so such a choice would not be compatible with our design principle of conservativity over Standard ML. Finally, unlike SMLSC, O’Caml and its separate compilation system are defined informally in terms of their implementation.

Moscow ML. The Moscow ML [20] compiler for Standard ML supports a separate compilation system nearly identical to Objective Caml’s. Moscow ML extends the Standard ML module system to allow (among other things) **functor** and **signature** declarations in structures and specifications for them in signatures. Then, like O’Caml, units are structures. In contrast, SMLSC does not require any changes to the Standard ML module language.

Other Standard ML implementations include mechanisms for breaking programs up into compilation units. None support separate compilation in the sense we use it here; they use the term to mean cut-off incremental recompilation (recall Section 5).

SML/NJ CM. The Compilation Manager for Standard ML of New Jersey (CM) [3] is a tool for compiling Standard ML programs spread across many source files. CM permits a program to be divided into a hierarchy of libraries [4]. A library comprises a list of imported libraries, Standard ML source files, and a list of symbols exported by the library. Dependencies between libraries are explicit but dependencies among the source files in a library are inferred [2, 9]. CM provides control over the identifiers visible to a source file, and supports conditional compilation, parallel compilation, and cut-off incremental recompilation. CM provides no way for the programmer to write interfaces nor to compile against unimplemented units. SMLSC is not a replacement for CM; we believe that dependency analysis and recompilation tools are useful, and that SMLSC provides a good linguistic target for such tools.

ML Basis. The MLton compiler [18] and ML Kit [17] implement a language called ML Basis. A “basis” in their terminology is what we call a unit. An ML Basis program is a series of declarations, including a binding construct for bases and an **open** construct for basis identifiers. These are analogous to SMLSC’s unit declaration and IC **import** declaration. Like SMLSC, the order of compilation entities is explicit, and thus each program has unambiguous meaning. ML Basis is given a formal semantics [6] in terms of *The Definition of Standard ML*. The implementation

of ML Basis in the ML Kit supports cut-off incremental recompilation based on Elsmann's thesis work [7]. Like CM, ML Basis does not provide a way for programmers to write down interfaces or separately compile against unimplemented bases.

8 Conclusion

We have presented an extension to Standard ML for separate compilation called SMLSC. Its focus is the *unit*, a program fragment that can depend on other program fragments through either separate or incremental compilation. Via the programming idiom of handoff units—that uses both separate and incremental compilation—we limit the number and complexity of linguistic mechanisms while supporting a convenient programming style. Our formal and abstract definition of the language ensures that it is unambiguously specified, and admits a variety of implementation strategies.

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A TS Linking Rules

The typed semantics defines a closed structure $mod_{basis}:sig_{basis}$ serving as an initial basis for the TSIL. The elaborator assumes Γ declares $basis:sig_{basis}$, which includes components such as the built-in `Match` exception. This basis structure is introduced in Rule 6 for completion and Rule 12 for elaboration of source units in projects.

We use the following definitions and notation.

- Writing $BV(dec)$ for the variable declared by dec , we define the bound variables of a structure declaration list, $BV(sdecs)$, by

$$BV(lab_1 \triangleright dec_1, \dots, lab_n \triangleright dec_n) = \{BV(dec_1), \dots, BV(dec_n)\}.$$

A linkset

$$L = sdecs_0 \rightarrow sbnds : sdecs; S$$

binds variables $BV(sdecso)$ with scope $sbinds : sdecso; S$ and variables $BV(sdecso)$ with scope S . We write $BV(L)$ for $BV(sdecso) \cup BV(sdecso)$.

- For readability, we sometimes elide variables in structure bindings and declarations. It should be immediately obvious how to consistently restore these with fresh variables.
- We assume that unit identifiers are disjoint from all other identifier classes.
- We assume that the TS overbar injection $\bar{\cdot}$ maps identifiers of different classes to different labels and that there are infinitely many labels not in its range.

We assume that its range includes neither the distinguished label **basis**, nor the labels chosen fresh in the rules.

- Structure declaration lists $sdecso$, signature abbreviations S , and so on specify lists of elements. We adopt the following notation for lists.
 - We denote by (\cdot, \cdot) the operation of syntactic concatenation; for example, S, S' .
 - We sometimes use pattern matching at the left end of a list, writing $sigid=sig, S$ to match the first binding in the list.
 - We usually omit the initial \cdot ; for example,

$$sigid_1=sig_1, \dots, sigid_1=sig_1.$$

- We define the domain of a signature abbreviation, $\text{dom}(S)$, by

$$\begin{aligned} \text{dom}(\cdot) &= \emptyset \\ \text{dom}(S, sigid=sig) &= \text{dom}(S) \cup \{sigid\} \\ \text{dom}(S, unitid=S_u) &= \text{dom}(S) \cup \{unitid\}. \end{aligned}$$

- We define the function $S++S'$ by

$$\begin{aligned} (\cdot ++ S') &= S' \\ ((sigid=sig, S) ++ S') &= \begin{cases} sigid=sig, S'' & \text{if } sigid \notin \text{dom}(S'') \\ S'' & \text{otherwise} \end{cases} \\ &\text{where } S'' = S ++ S' \\ ((unitid=S_u, S) ++ S') &= \begin{cases} unitid=S_u, S'' & \text{if } unitid \notin \text{dom}(S'') \\ S'' & \text{otherwise} \end{cases} \\ &\text{where } S'' = S ++ S'. \end{aligned}$$

It concatenates S and S' , making the result well-formed by dropping signature abbreviations if $\text{dom}(S) \cap \text{dom}(S') \neq \emptyset$.

- We write both “=” and “:=” in side-conditions. Interpreting the rules algorithmically, the former pattern-matches inputs, and the latter specifies an output.

$$\boxed{decs \vdash L \text{ ok}}$$

$$\frac{\begin{array}{c} decs \vdash sdecs_0 \text{ ok} \\ decs, sdecs_0 \vdash sbnds : sdecs \\ decs, sdecs_0, sdecs \vdash S \text{ ok} \\ sdecs_0 = lab_1 \triangleright var_1 : [sdecs_1], \dots, lab_n \triangleright var_n : [sdecs_n] \end{array}}{decs \vdash sdecs_0 \rightarrow sbnds : sdecs; S \text{ ok}} \quad (1)$$

Rule 1: Imports are restricted to structures. The elaborator in Appendix B needs nothing else.

$$\boxed{decs \vdash S \text{ ok}}$$

$$\frac{\vdash decs \text{ ok}}{decs \vdash \cdot \text{ ok}} \quad (2)$$

$$\frac{decs \vdash sig : \text{Sig} \quad decs \vdash S \text{ ok} \quad sigid \notin \text{dom}(S)}{decs \vdash sigid = sig, S \text{ ok}} \quad (3)$$

$$\frac{\begin{array}{c} decs \vdash S' \text{ ok} \quad decs \vdash S \text{ ok} \quad unitid \notin \text{dom}(S) \\ S' = (sigid_1 = sig_1, \dots, sigid_n = sig_n) \end{array}}{decs \vdash unitid = S', S \text{ ok}} \quad (4)$$

$$\boxed{L \rightsquigarrow exp : \{\}}$$

$$\frac{lab \notin \text{dom}(sdecs)}{\cdot \rightarrow sbnds : sdecs; S \rightsquigarrow [sbnds, lab = \{\}].lab : \{\}} \quad (5)$$

$$\frac{\begin{array}{c} L_{basis} := \cdot \rightarrow \text{basis} = mod_{basis} : \text{basis}; sig_{basis}; \cdot \\ \vdash L_{basis} ++ L \rightsquigarrow L' \quad L' \rightsquigarrow exp : \{\} \end{array}}{L \rightsquigarrow exp : \{\}} \quad (6)$$

$$\boxed{decs \vdash L ++ L' \rightsquigarrow L''}$$

$$\frac{L = sdecs_0 \rightarrow sbnds : sdecs; S}{decs \vdash L ++ (\cdot \rightarrow sbnds' : sdecs'; S') \rightsquigarrow sdecs_0 \rightarrow sbnds ++ sbnds' : sdecs ++ sdecs'; S ++ S'} \quad (7)$$

$$\frac{\begin{array}{c} sdecs = sdecs'', lab \triangleright var' : sig', sdecs''' \\ decs, sdecs_0, sdecs \vdash_{\text{sub}} var' : sig' \preceq sig \rightsquigarrow mod : sig'' \\ sbnd := 1 \triangleright var = mod \quad sdec := 1 \triangleright var : sig'' \\ L := sdecs_0 \rightarrow sbnds ++ sbnd : sdecs ++ sdec; S \\ decs \vdash L ++ (sdecs_1 \rightarrow sbnds' : sdecs'; S') \rightsquigarrow L'' \end{array}}{decs \vdash (sdecs_0 \rightarrow sbnds : sdecs; S) ++ (lab \triangleright var : sig, sdecs_1 \rightarrow sbnds' : sdecs'; S') \rightsquigarrow L''} \quad (8)$$

$$\begin{array}{c}
lab \notin \text{dom}(sdec s) \\
sdec s_0 = sdec s'', lab \triangleright var':sig', sdec s''' \\
dec s, sdec s_0, sdec s \vdash sig \equiv sig' : \text{Sig} \\
L' := \{var'/var\}(sdec s_1 \rightarrow sbnds' : sdec s'; S') \\
dec s \vdash (sdec s_0 \rightarrow sbnds : sdec s; S) ++ L' \rightsquigarrow L'' \\
\hline
dec s \vdash (sdec s_0 \rightarrow sbnds : sdec s; S) ++ \\
(lab \triangleright var:sig, sdec s_1 \rightarrow sbnds' : sdec s'; S') \rightsquigarrow L''
\end{array} \tag{9}$$

$$\begin{array}{c}
lab \notin \text{dom}(sdec s) \cup \text{dom}(sdec s_0) \\
dec s, sdec s_0, sdec s \vdash sig \equiv sig' : \text{Sig} \\
dec s, sdec s_0 \vdash sig' : \text{Sig} \\
L := sdec s_0, lab \triangleright var:sig' \rightarrow sbnds : sdec s; S \\
dec s \vdash L ++ (sdec s_1 \rightarrow sbnds' : sdec s'; S') \rightsquigarrow L'' \\
\hline
dec s \vdash (sdec s_0 \rightarrow sbnds : sdec s; S) ++ \\
(lab \triangleright var:sig, sdec s_1 \rightarrow sbnds' : sdec s'; S') \rightsquigarrow L''
\end{array} \tag{10}$$

B TS Elaboration Rules

We change the TS elaborator to expand signature abbreviations. First, we modify every TS elaboration judgement and rule using a TS elaboration context $sdec$ s to use $sdec$ s; S . A context $sdec$ s; S binds variables $BV(sdec$ s) with scope S . We define $BV(\Gamma)$ by $BV(sdec$ s). Second, we extend the syntax for TSEL signature expressions:

$$\begin{aligned} sigexp & ::= \dots \\ & \quad sigid \quad \text{signature identifier} \end{aligned}$$

Finally, we extend the TS judgement $\Gamma \vdash sigexp \rightsquigarrow sig : \text{Sig}$, adding the rule

$$\frac{\Gamma \vdash_{\text{ctx}} sigid \rightsquigarrow sig : \text{Sig}}{\Gamma \vdash sigid \rightsquigarrow sig : \text{Sig}}$$

to elaborate signature identifiers.

We use the following definitions and notation.

- To extend an elaboration context $\Gamma = sdec$ s; S , we write

$$\begin{aligned} \Gamma, dec & \text{ for } sdec$$
s, $1 \triangleright dec$; S ,
 $\Gamma, sdec$ s' & \text{ for } sdecs, $sdec$ s'; S , and
 Γ, S' & \text{ for } sdecs; S, S' .
\end{aligned}

We also define a function $R(sdec$ s) that renames the labels in $sdec$ s to make them inaccessible to identifier resolution:

$$R(lab_1 \triangleright dec_1, \dots, lab_n \triangleright dec_n) = 1 \triangleright dec_1, \dots, 1 \triangleright dec_n.$$

- We define a function $U(sdec$ s) that drops the labels in $sdec$ s:

$$U(lab_1 \triangleright dec_1, \dots, lab_n \triangleright dec_n) = dec_1, \dots, dec_n.$$

- When an elaboration context $\Gamma = sdec$ s; S appears in a judgement requiring an IL context dec s, we implicitly coerce Γ to $U(sdec$ s).
- We define the substitution function $\sigma(var, sdec$ s, S) by

$$\begin{aligned} \sigma(var, \cdot, S) &= S \\ \sigma(var, (lab \triangleright dec, sdec$$
s), S) &= \\ & \{var.lab / BV(dec)\} \sigma(var, sdecs, S) \end{aligned}

where $\{path/var\}S$ denotes the capture-free substitution of $path$ for free occurrences of var in S . Rule 14 uses σ to elaborate source units.

$project \rightsquigarrow L$

$$\frac{}{\cdot \rightsquigarrow (\cdot \rightarrow \cdot : \cdot ; \cdot)} \quad (11)$$

$$\begin{array}{c}
\text{project} \rightsquigarrow L \quad \text{basis} \notin \text{BV}(L) \\
L = \text{sdecs}_0 \rightarrow \text{sbnds} : \text{sdecs}; S \\
\Gamma := \text{basis} : \text{sig}_{\text{basis}}, R(\text{sdecs}_0), \text{sdecs}; S \\
\Gamma \vdash \text{srcunit} \rightsquigarrow L' \quad \text{var} \notin \text{BV}(L') \\
\text{sdecs}'_1 \rightarrow \text{sbnds}' : \text{sdecs}'; S' := \{\text{var}/\text{basis}\}L' \\
\text{sdecs}_1 := \text{basis} \triangleright \text{var} : \text{sig}_{\text{basis}}, \text{sdecs}'_1 \\
\vdash L ++ (\text{sdecs}_1 \rightarrow \text{sbnds}' : \text{sdecs}'; S') \rightsquigarrow L'' \\
\hline
\text{project}, \text{srcunit} \rightsquigarrow L''
\end{array} \tag{12}$$

Rule 12: The side-condition $\text{basis} \notin \text{BV}(L)$ can always be achieved by renaming bound variables in L .

$$\begin{array}{c}
\text{project} \rightsquigarrow L \\
\text{BV}(L) \cap \text{BV}(L') = \emptyset \quad \vdash L' \text{ ok} \quad \vdash L ++ L' \rightsquigarrow L'' \\
\hline
\text{project}, L' \rightsquigarrow L''
\end{array} \tag{13}$$

$$\boxed{\Gamma \vdash \text{srcunit} \rightsquigarrow L}$$

$$\begin{array}{c}
\Gamma \vdash \text{topdec} \rightsquigarrow L \\
L = \text{sdecs}_0 \rightarrow \text{sbnds} : \text{sdecs}; S \\
\text{var} \notin \text{BV}(\Gamma) \cup \text{BV}(L) \\
\text{sbnds}' := \overline{\text{unitid}} \triangleright \text{var} = [\text{sbnds}] \\
\text{sdecs}' := \overline{\text{unitid}} \triangleright \text{var} : [\text{sdecs}] \\
S' := \text{unitid} = \sigma(\text{var}, \text{sdecs}, S) \\
\hline
\Gamma \vdash \text{unit } \text{unitid} = \text{topdec} \rightsquigarrow \\
\text{sdecs}_0 \rightarrow \text{sbnds}' : \text{sdecs}'; S'
\end{array} \tag{14}$$

$$\boxed{\Gamma \vdash \text{topdec} \rightsquigarrow L}$$

$$\begin{array}{c}
\Gamma \vdash \text{impexp} \rightsquigarrow L \\
\hline
\Gamma \vdash \text{import } \text{impexp} \rightsquigarrow L
\end{array} \tag{15}$$

$$\begin{array}{c}
\Gamma \vdash \text{strdec} \rightsquigarrow \text{sbnds} : \text{sdecs} \\
\hline
\Gamma \vdash \text{strdec} \rightsquigarrow \cdot \rightarrow \text{sbnds} : \text{sdecs}; \cdot
\end{array} \tag{16}$$

$$\begin{array}{c}
\Gamma \vdash \text{sigbind} \rightsquigarrow S \\
\hline
\Gamma \vdash \text{signature } \text{sigbind} \rightsquigarrow \cdot \rightarrow \cdot : \cdot; S
\end{array} \tag{17}$$

$$\begin{array}{c}
\Gamma \vdash \text{topdec} \rightsquigarrow \text{sdecs}_0 \rightarrow \text{sbnds} : \text{sdecs}; S \\
\text{var} \notin \text{BV}(\Gamma) \cup \text{BV}(\text{sdecs}_0) \\
\Gamma, R(\text{sdecs}_0), 1^* \triangleright \text{var} : [\text{sdecs}], S \vdash \text{topdec}' \rightsquigarrow L' \\
L := \text{sdecs}_0 \rightarrow 1 \triangleright \text{var} = [\text{sbnds}] : 1 \triangleright \text{var} : [\text{sdecs}]; \cdot \\
\Gamma \vdash L ++ L' \rightsquigarrow L''
\end{array} \tag{18}$$

$$\Gamma \vdash \text{local topdec in topdec}' \text{ end} \rightsquigarrow L''$$

$$\begin{array}{c}
\Gamma \vdash \text{topdec} \rightsquigarrow L \\
L = \text{sdecs}_0 \rightarrow \text{sbnds} : \text{sdecs}; S \\
\Gamma, R(\text{sdecs}_0), \text{sdecs}, S \vdash \text{topdec}' \rightsquigarrow L' \\
\Gamma \vdash L ++ L' \rightsquigarrow L''
\end{array} \tag{19}$$

$$\Gamma \vdash \text{topdec topdec}' \rightsquigarrow L''$$

$\Gamma \vdash \text{impexp} \rightsquigarrow L$

$$\begin{array}{c}
\Gamma \vdash_{\text{ctx}} \overline{\text{unitid}} \rightsquigarrow \text{var} : \text{sig} \\
\Gamma \vdash_{\text{ctx}} \text{unitid} \rightsquigarrow S \quad \text{var}' \notin \text{BV}(\Gamma) \\
L := \overline{\text{unitid}} \triangleright \text{var}' : \text{sig} \rightarrow 1^* = \text{var}' : 1^* : \text{sig}; \cdot
\end{array} \tag{20}$$

$$\Gamma \vdash \text{unitid} \rightsquigarrow L$$

Rule 20: Rules 12, 18, and 19 use $R(\cdot)$ to hide imported units from IR imports. The signature sig should be fully selfified.

$$\begin{array}{c}
\Gamma \vdash \text{spec} \rightsquigarrow \text{sdecs} \quad \text{var}' \notin \text{BV}(\Gamma) \\
\Gamma, \text{var}' : [\text{sdecs}] \vdash \text{var}' : \text{sig} \\
L := \overline{\text{unitid}} \triangleright \text{var}' : [\text{sdecs}] \rightarrow 1^* = \text{var}' : 1^* : \text{sig}; \cdot
\end{array} \tag{21}$$

$$\Gamma \vdash \text{unitid} : \text{intf spec end} \rightsquigarrow L$$

Rule 21: The signature sig should be fully selfified.

$$\begin{array}{c}
\Gamma \vdash \text{impexp} \rightsquigarrow L \quad \Gamma \vdash \text{impexp}' \rightsquigarrow L' \\
\text{BV}(L) \cap \text{BV}(L') = \emptyset \quad \Gamma \vdash L ++ L' \rightsquigarrow L''
\end{array} \tag{22}$$

$$\Gamma \vdash \text{impexp impexp}' \rightsquigarrow L''$$

$\Gamma \vdash \text{sigbind} \rightsquigarrow S$

$$\begin{array}{c}
\Gamma \vdash \text{sigexp} \rightsquigarrow \text{sig} : \text{Sig} \quad S := \text{sigid} = \text{sig} \\
\langle \Gamma \vdash \text{sigbind} \rightsquigarrow S' \quad \text{sigid} \notin \text{dom}(S') \rangle
\end{array} \tag{23}$$

$$\Gamma \vdash \text{sigid} = \text{sigexp} \langle \text{and sigbind} \rangle \rightsquigarrow S \langle, S' \rangle$$

Rule 23: Either all optional elements or none must be present.

$$\boxed{\Gamma \vdash_{\text{ctx}} \text{sigid} \rightsquigarrow \text{sig} : \text{Sig}}$$

$$\frac{}{sdecs; S, \text{sigid}=\text{sig} \vdash \text{sigid} \rightsquigarrow \text{sig} : \text{Sig}} \quad (24)$$

$$\frac{\begin{array}{c} \text{sigid}' \neq \text{sigid} \\ sdecs; S \vdash \text{sigid} \rightsquigarrow \text{sig} : \text{Sig} \end{array}}{sdecs; S, \text{sigid}'=\text{sig}' \vdash \text{sigid} \rightsquigarrow \text{sig} : \text{Sig}} \quad (25)$$

$$\frac{sdecs; S \vdash \text{sigid} \rightsquigarrow \text{sig} : \text{Sig}}{sdecs; S, \text{unitid}=S' \vdash \text{sigid} \rightsquigarrow \text{sig} : \text{Sig}} \quad (26)$$

$$\boxed{\Gamma \vdash_{\text{ctx}} \text{unitid} \rightsquigarrow S}$$

$$\frac{}{sdecs; S, \text{unitid}=S' \vdash \text{unitid} \rightsquigarrow S'} \quad (27)$$

$$\frac{\begin{array}{c} \text{unitid}' \neq \text{unitid} \\ sdecs; S \vdash \text{unitid} \rightsquigarrow S'' \end{array}}{sdecs; S, \text{unitid}'=S' \vdash \text{unitid} \rightsquigarrow S''} \quad (28)$$

$$\frac{sdecs; S \vdash \text{unitid} \rightsquigarrow S'}{sdecs; S, \text{sigid}=\text{sig} \vdash \text{unitid} \rightsquigarrow S'} \quad (29)$$

$$\boxed{\Gamma \text{ ok}}$$

$$\frac{\vdash U(sdecs) \text{ ok}}{sdecs; \cdot \text{ ok}} \quad (30)$$

$$\frac{sdecs; S \text{ ok} \quad sdecs \vdash \text{sig} : \text{Sig}}{sdecs; S, \text{sigid}=\text{sig} \text{ ok}} \quad (31)$$

$$\frac{\begin{array}{c} sdecs; S \text{ ok} \quad sdecs \vdash S' \text{ ok} \\ S' = (\text{sigid}_1=\text{sig}_1, \dots, \text{sigid}_n=\text{sig}_n) \end{array}}{sdecs; S, \text{unitid}=S' \text{ ok}} \quad (32)$$

B or T, F, G, E	\in	$\text{Basis} = \text{TyNameSet} \times \text{FunEnv} \times \text{SigEnv} \times \text{Env}$
T	\in	$\text{TyNameSet} = \text{Fin}(\text{TyName})$
F	\in	$\text{FunEnv} = \text{FunId} \xrightarrow{\text{fin}} \text{FunSig}$
G	\in	$\text{SigEnv} = \text{SigId} \xrightarrow{\text{fin}} \text{Sig}$
E or (SE, TE, VE)	\in	$\text{Env} = \text{StrEnv} \times \text{TyEnv} \times \text{ValEnv}$
Φ or $(T)(E, (T')E')$	\in	$\text{FunSig} = \text{TyNameSet} \times (\text{Env} \times \text{Sig})$
Σ or $(T)E$	\in	$\text{Sig} = \text{TyNameSet} \times \text{Env}$
SE	\in	$\text{StrEnv} = \text{StrId} \xrightarrow{\text{fin}} \text{Env}$
TE	\in	$\text{TyEnv} = \text{TyCon} \xrightarrow{\text{fin}} \text{TyStr}$
VE	\in	$\text{ValEnv} = \text{VId} \xrightarrow{\text{fin}} \text{TypeScheme} \times \text{IdStatus}$
t	\in	TyName (type names)
$funid$	\in	FunId (functor identifiers)
$sigid$	\in	SigId (signature identifiers)
$strid$	\in	StrId (structure identifiers)
$tycon$	\in	TyCon (type constructors)
vid	\in	VId (value identifiers)

Figure 20: Static TDIL for Standard ML (summary). $\text{Fin}(A)$ denotes the set of finite subsets of A

C Unit Static Semantic Rules

We use the following definitions and notation.

- **TD’s static semantics.** We recall the static TDIL for Standard ML in Figure 20.

We write E_\emptyset for the empty environment $(\{\}, \{\}, \{\})$.

We write $\text{tynames } A$ for the set of free type names in the semantic object A .

We write $\text{tyvars } A$ for the set of free type variables in the semantic object A [16, Section 4.2].

For any semantic object A , we write $A \text{ wf}$ for “every type structure occurring in A is well-formed” [16, Section 4.9].

We assume familiarity with realizations φ and their support $\text{Supp } \varphi$ [16, Section 5.2].

We assume familiarity with signature instantiation and enrichment [16, Sections 5.3 and 5.5].

- **Sets.** We write $A \setminus B$ for $\{a \in A ; a \notin B\}$.

We write $\#A$ for the cardinality of the finite set A .

- **Finite maps.** The domain of a finite map $f : A \xrightarrow{\text{fin}} B$ is the set $\text{dom}(f)$ of those $a \in A$ for which f is defined.

Finite maps may be specified explicitly; for example $\{a_1 \mapsto b_1, \dots, a_n \mapsto b_n\}$ ($n \geq 0$).

We extend this notation to a form of set comprehension, writing $\{a \mapsto b ; \phi\}$ for the map taking every a satisfying ϕ to $b(a)$.

- **Extension.** To extend finite maps $f, g : A \xrightarrow{\text{fin}} B$, we define $f + g : A \xrightarrow{\text{fin}} B$ by

$$(f + g)(a) = \begin{cases} g(a) & \text{if } a \in \text{dom}(g) \\ f(a) & \text{otherwise.} \end{cases}$$

We define $T + T'$ by $T \cup T'$.

We extend semantic objects componentwise; for example, $E + E'$ and $B + B'$ have the evident definitions.

We extend components in a semantic object when it is unambiguous to do so; for example,

$$\begin{aligned}\Gamma + U &= B, U' + U \\ \Gamma + B' &= B + B', U' \\ \Gamma + T &= B + T, U' = (T' + T, F, G, E), U'\end{aligned}$$

where $\Gamma = B, U'$ and $B = T', F, G, E$.

We lift application to finite maps in semantic objects when it is unambiguous to do so; for example,

$$\begin{aligned}\Gamma(\text{unitid}) &= U(\text{unitid}) \\ B(\text{strid}) &= E(\text{strid}) = SE(\text{strid})\end{aligned}$$

where $\Gamma = B, U$; $B = T, F, G, E$; and $E = (SE, TE, VE)$.

- **Projection.** We write $(\cdot \text{ of } \cdot)$ for projection from semantic objects; for example,

$$T \text{ of } \Gamma = T \text{ of } B = T'$$

where $\Gamma = B, U$ and $B = T', F, G, E$.

$$\boxed{\Gamma \vdash \text{udecs} : \text{uspecs}}$$

$$\frac{\Gamma \text{ ok}}{\Gamma \vdash \cdot : \cdot} \quad (33)$$

$$\frac{\begin{array}{l} \text{srcunit} = (\text{unit } \text{unitid} = \text{unit } \text{topdec } \text{end}) \quad \Upsilon = (T)(F, G, E) \\ \Gamma \vdash \text{topdec} : T, F, G, E \\ \Gamma + T + \{\text{unitid} \mapsto T, F, G, E\} \vdash \text{udecs} : \text{uspecs} \end{array}}{\Gamma \vdash (\text{srcunit}, \text{udecs}) : (\text{unitid} : \Upsilon, \text{uspecs})} \quad (34)$$

$$\boxed{\Gamma \vdash \text{topdec} : B}$$

$$\frac{\Gamma \vdash \text{impexp} : F, G, E}{\Gamma \vdash \text{import } \text{impexp} : \emptyset, F, G, E} \quad (35)$$

$$\frac{B \vdash \text{strdec} \Rightarrow E \quad T := \text{tynames } E \setminus (T \text{ of } B) \quad \text{tyvars } E = \emptyset}{B, U \vdash \text{strdec} : T, \{\}, \{\}, E} \quad (36)$$

$$\frac{B \vdash \text{sigdec} \Rightarrow G}{B, U \vdash \text{sigdec} : \emptyset, \{\}, G, E_{\emptyset}} \quad (37)$$

Rule 37: TD's Rules 65 and 67 ensure that tynames $G = \emptyset$. TD's rules for $B \vdash \text{spec} \Rightarrow E$ ensure that tyvars $G = \emptyset$.

$$\frac{B \vdash \text{fundec} \Rightarrow F \quad T := \text{tynames } F \setminus (T \text{ of } B) \quad \text{tyvars } F = \emptyset}{B, U \vdash \text{fundec} : T, F, \{\}, E_\emptyset} \quad (38)$$

$$\frac{\Gamma \vdash \text{topdec} : B \quad \Gamma + B \vdash \text{topdec}' : T', F', G', E' \quad T := (T \text{ of } B) \cup T'}{\Gamma \vdash \text{local } \text{topdec} \text{ in } \text{topdec}' \text{ end} : T, F', G', E'} \quad (39)$$

$$\frac{\Gamma \vdash \text{topdec} : B \quad \Gamma + B \vdash \text{topdec}' : B'}{\Gamma \vdash \text{topdec } \text{topdec}' : B + B'} \quad (40)$$

$$\boxed{\Gamma \vdash \text{impexp} : F, G, E}$$

$$\frac{\Gamma(\text{unitid}) = T, F, G, E}{\Gamma \vdash \text{unitid} : F, G, E} \quad (41)$$

$$\frac{\begin{array}{l} B \text{ of } \Gamma \vdash \text{intexp} \Rightarrow \Upsilon \quad \Upsilon = (T)(F, \{\}, E) \\ \Gamma(\text{unitid}) = T', F', G', E' \quad F', G', E' : \Upsilon \text{ using } \varphi \end{array}}{\Gamma \vdash (\text{unitid} : \text{intexp}) : \varphi(F), \{\}, \varphi(E)} \quad (42)$$

$$\frac{\Gamma \vdash \text{impexp} : F, G, E \quad \Gamma \vdash \text{impexp}' : F', G', E'}{\Gamma \vdash \text{impexp } \text{impexp}' : F + F', G + G', E + E'} \quad (43)$$

$$\boxed{B \vdash \text{intexp} \Rightarrow \Upsilon}$$

$$\frac{B \vdash \text{topspec} \Rightarrow F, E \quad T := (\text{tynames } F, E) \setminus (T \text{ of } B)}{B \vdash \text{intf } \text{topspec} \text{ end} \Rightarrow (T)(F, \{\}, E)} \quad (44)$$

$$\boxed{B \vdash \text{topspec} \Rightarrow F, E}$$

$$\frac{B \vdash \text{spec} \Rightarrow E}{B \vdash \text{spec} \Rightarrow \{\}, E} \quad (45)$$

$$\frac{B \vdash \text{funspec} \Rightarrow F}{B \vdash \text{functor } \text{funspec} \Rightarrow F, \{\}} \quad (46)$$

$$\frac{\begin{array}{l} B \vdash \text{topspec} \Rightarrow F, E \quad B + E \vdash \text{topspec}' \Rightarrow F', E' \\ \text{dom}(F) \cap \text{dom}(F') = \emptyset \quad \text{dom}(E) \cap \text{dom}(E') = \emptyset \end{array}}{B \vdash \text{topspec } \text{topspec}' \Rightarrow F + F', E + E'} \quad (47)$$

$$\boxed{B \vdash \text{funspec} \Rightarrow F}$$

$$\frac{\begin{array}{c} B \vdash \text{sigexp} \Rightarrow (T)E \quad B + T + \{\text{strid} \mapsto E\} \vdash \text{sigexp}' \Rightarrow (T')E' \\ F := \{\text{funid} \mapsto (T)(E, (T')E')\} \\ \langle B \vdash \text{funspec} \Rightarrow F' \quad \text{funid} \notin \text{dom}(F') \rangle \end{array}}{B \vdash \text{funid}(\text{strid} : \text{sigexp}) : \text{sigexp}' \langle \text{and funspec} \rangle \Rightarrow F \langle + F' \rangle} \quad (48)$$

$$\boxed{\Gamma \text{ ok}}$$

$$\frac{\text{tynames } F, G, E, U \subset T \quad F, G, E, U \text{ wf}}{(T, F, G, E), U \text{ ok}} \quad (49)$$

$$\boxed{\Gamma \vdash \Upsilon : \text{Sig}}$$

$$\frac{\begin{array}{c} T = T \text{ of } \Gamma \\ T \cap T' = \emptyset \quad \text{tynames } F, G, E \subset T \cup T' \\ \text{tyvars } F, G, E = \emptyset \quad F, G, E \text{ wf} \end{array}}{\Gamma \vdash (T')(F, G, E) : \text{Sig}} \quad (50)$$

$$\boxed{\Gamma \vdash \text{uspecs ok}}$$

$$\frac{\Gamma \text{ ok}}{\Gamma \vdash \cdot \text{ ok}} \quad (51)$$

$$\frac{\Gamma \vdash \Upsilon : \text{Sig} \quad \Upsilon = (T')(F, G, E) \quad \Gamma + T' \vdash \text{uspecs ok}}{\Gamma \vdash \text{unitid}:\Upsilon, \text{uspecs ok}} \quad (52)$$

$$\boxed{E : \Sigma \text{ using } \varphi}$$

$$\frac{\Sigma \geq E^- \text{ using } \varphi \quad E \succ E^-}{E : \Sigma \text{ using } \varphi} \quad (53)$$

$$\boxed{\Sigma \equiv \Sigma' \text{ using } \varphi}$$

$$\frac{T = \varphi(T') \quad E = \varphi(E') \quad \text{Supp } \varphi \subset T' \quad \#T = \#T'}{(T)E \equiv (T')E' \text{ using } \varphi} \quad (54)$$

$$\boxed{\Upsilon \equiv \Upsilon' \text{ using } \varphi}$$

$$\frac{(T)E \equiv (T')E' \text{ using } \varphi \quad F = \varphi(F') \quad G = \varphi(G')}{(T)(F, G, E) \equiv (T')(F', G', E') \text{ using } \varphi} \quad (55)$$

$$\boxed{F, G, E : \Upsilon \text{ using } \varphi}$$

$$\frac{\begin{array}{l} E : (T)E' \text{ using } \varphi \\ \text{dom}(F) \supset \text{dom}(F') \\ \forall \text{funid} \in \text{dom}(F'). \exists \varphi_1, \varphi_2 \left\{ \begin{array}{l} E'_1 : (T_1)E_1 \text{ using } \varphi_1 \\ \varphi_1(E_2) : (T'_2)E'_2 \text{ using } \varphi_2 \\ \text{where } (T'_1)(E'_1, (T'_2)E'_2) = \varphi(F'(\text{funid})) \\ \text{and } (T_1)(E_1, (T_2)E_2) = F(\text{funid}) \end{array} \right. \\ \text{dom}(G) \supset \text{dom}(G') \\ \forall \text{sigid} \in \text{dom}(G'). \exists \varphi'. G(\text{sigid}) \equiv \varphi(G'(\text{sigid})) \text{ using } \varphi' \end{array}}{F, G, E : (T)(F', G', E') \text{ using } \varphi} \quad (56)$$

$$\begin{array}{ll}
(F, G, E) \text{ or } B & \in \text{Basis} = \text{FunEnv} \times \text{SigEnv} \times \text{Env} \\
F & \in \text{FunEnv} = \text{FunId} \xrightarrow{\text{fin}} \text{FunctorClosure} \\
G & \in \text{SigEnv} = \text{SigId} \xrightarrow{\text{fin}} \text{Int} \\
(SE, TE, VE) \text{ or } E & \in \text{Env} = \text{StrEnv} \times \text{TyEnv} \times \text{ValEnv} \\
(strid : I, strexp, B) & \in \text{FunctorClosure} = (\text{StrId} \times \text{Int}) \times \text{StrExp} \times \text{Basis} \\
(SI, TI, VI) \text{ or } I & \in \text{Int} = \text{StrInt} \times \text{TyInt} \times \text{ValInt} \\
SE & \in \text{StrEnv} = \text{StrId} \xrightarrow{\text{fin}} \text{Env} \\
TE & \in \text{TyEnv} = \text{TyCon} \xrightarrow{\text{fin}} \text{ValEnv} \\
VE & \in \text{ValEnv} = \text{VId} \xrightarrow{\text{fin}} \text{Val} \times \text{IdStatus} \\
SI & \in \text{StrInt} = \text{StrId} \xrightarrow{\text{fin}} \text{Int} \\
TI & \in \text{TyInt} = \text{TyCon} \xrightarrow{\text{fin}} \text{ValInt} \\
VI & \in \text{ValInt} = \text{VId} \xrightarrow{\text{fin}} \text{IdStatus} \\
(G, I) \text{ or } IB & \in \text{IntBasis} = \text{SigEnv} \times \text{Int}
\end{array}$$

Figure 21: Dynamic TDIL for Standard ML (summary)

D Unit Dynamic Semantic Rules

We use the following definitions and notation.

- We recall the dynamic TDIL for Standard ML in Figure 21.
- We extend the TDIL category FunctorClosure as follows, permitting an optional ascribed interface on the functor body.

$$\begin{array}{ll}
& \text{FunctorClosure} = \text{FClos} \cup \text{FClos}' \\
(strid : I, strexp, B) & \in \text{FClos} = (\text{StrId} \times \text{Int}) \times \text{StrExp} \times \text{Basis} \\
(strid : I, strexp : I', B) & \in \text{FClos}' = (\text{StrId} \times \text{Int}) \times (\text{StrExp} \times \text{Int}) \times \text{Basis}.
\end{array}$$

Here, $A \cup B$ denotes the disjoint union of A and B .

- We define the function $\downarrow : \text{Basis} \times \text{UnitInt} \rightarrow \text{Basis}$ that cuts down a basis B to match a unit interface UI :

$$\begin{array}{l}
\downarrow : \text{Basis} \times \text{UnitInt} \rightarrow \text{Basis} \\
(F, G, E) \downarrow (FI, I) = (F \downarrow FI, \{\}, E \downarrow I) \\
\\
\downarrow : \text{FunEnv} \times \text{FunIntEnv} \rightarrow \text{FunEnv} \\
F \downarrow FI = \{funid \mapsto F(funid) \downarrow FI(funid) ; funid \in \text{dom}(F) \cap \text{dom}(FI)\} \\
\\
\downarrow : \text{FunctorClosure} \times (\text{Int} \times \text{Int}) \rightarrow \text{FunctorClosure} \\
(strid:I_0, strexp, B) \downarrow (I, I') = (strid:I, strexp:I', B) \\
(strid:I_0, strexp:I'_0, B) \downarrow (I, I') = (strid:I, strexp:I', B).
\end{array}$$

- TD's evaluation judgement $s, B \vdash strexp \Rightarrow E/p, s'$ handles functor application for functor closures of the form FClos. We extend that judgement by adding the following rules to handle

functor closures of the form FClos'.

$$\begin{array}{c}
\frac{B(\text{funid}) = (\text{strid}:I, \text{strexpr}':I', B') \quad s, B \vdash \text{strexpr} \Rightarrow E, s' \quad s', B' + \{\text{strid} \mapsto E \downarrow I\} \vdash \text{strexpr}' \Rightarrow E', s''}{s, B \vdash \text{funid}(\text{strexpr}) \Rightarrow E' \downarrow I', s''} \\
\\
\frac{B(\text{funid}) = (\text{strid}:I, \text{strexpr}':I', B') \quad s, B \vdash \text{strexpr} \Rightarrow p, s'}{s, B \vdash \text{funid}(\text{strexpr}) \Rightarrow p, s'} \\
\\
\frac{B(\text{funid}) = (\text{strid}:I, \text{strexpr}':I', B') \quad s, B \vdash \text{strexpr} \Rightarrow E, s' \quad s', B' + \{\text{strid} \mapsto E \downarrow I\} \vdash \text{strexpr}' \Rightarrow p, s''}{s, B \vdash \text{funid}(\text{strexpr}) \Rightarrow p, s''}
\end{array}$$

$$\boxed{s, \Gamma \vdash \text{udecs} \Rightarrow \Gamma'/p, s'}$$

$$\frac{}{s, \Gamma \vdash \cdot \Rightarrow \Gamma, s} \quad (57)$$

$$\frac{\text{srcunit} = (\text{unit unitid} = \text{unit topdec end}) \quad s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + \{\text{unitid} \mapsto B\} \vdash \text{udecs} \Rightarrow \Gamma', s''}{s, \Gamma \vdash \text{srcunit}, \text{udecs} \Rightarrow \Gamma', s''} \quad (58)$$

$$\frac{\text{srcunit} = (\text{unit unitid} = \text{unit topdec end}) \quad s, \Gamma \vdash \text{topdec} \Rightarrow p, s'}{s, \Gamma \vdash \text{srcunit}, \text{udecs} \Rightarrow p, s'} \quad (59)$$

$$\frac{\text{srcunit} = (\text{unit unitid} = \text{unit topdec end}) \quad s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + \{\text{unitid} \mapsto B\} \vdash \text{udecs} \Rightarrow p, s''}{s, \Gamma \vdash \text{srcunit}, \text{udecs} \Rightarrow p, s''} \quad (60)$$

$$\boxed{s, \Gamma \vdash \text{topdec} \Rightarrow B/p, s'}$$

$$\frac{\Gamma \vdash \text{impexp} \Rightarrow B}{s, \Gamma \vdash \text{import impexp} \Rightarrow B, s} \quad (61)$$

$$\frac{s, B \vdash \text{strdec} \Rightarrow E, s'}{s, (B, U) \vdash \text{strdec} \Rightarrow (\{\}, \{\}, E), s'} \quad (62)$$

$$\frac{s, B \vdash \text{strdec} \Rightarrow p, s'}{s, (B, U) \vdash \text{strdec} \Rightarrow p, s'} \quad (63)$$

$$\frac{\text{Inter } B \vdash \text{sigdec} \Rightarrow G}{s, (B, U) \vdash \text{sigdec} \Rightarrow (\{\}, G, E_\emptyset), s} \quad (64)$$

$$\frac{B \vdash \text{fundec} \Rightarrow F}{s, (B, U) \vdash \text{fundec} \Rightarrow (F, \{\}, E_\emptyset), s} \quad (65)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + B \vdash \text{topdec}' \Rightarrow B', s''}{s, \Gamma \vdash \text{local topdec in topdec}' \text{ end} \Rightarrow B', s''} \quad (66)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow p, s'}{s, \Gamma \vdash \text{local topdec in topdec}' \text{ end} \Rightarrow p, s'} \quad (67)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + B \vdash \text{topdec}' \Rightarrow p, s''}{s, \Gamma \vdash \text{local topdec in topdec}' \text{ end} \Rightarrow p, s''} \quad (68)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + B \vdash \text{topdec}' \Rightarrow B', s''}{s, \Gamma \vdash \text{topdec topdec}' \Rightarrow B + B', s''} \quad (69)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow p, s'}{s, \Gamma \vdash \text{topdec topdec}' \Rightarrow p, s'} \quad (70)$$

$$\frac{s, \Gamma \vdash \text{topdec} \Rightarrow B, s' \quad s', \Gamma + B \vdash \text{topdec}' \Rightarrow p, s''}{s, \Gamma \vdash \text{topdec topdec}' \Rightarrow p, s''} \quad (71)$$

$$\boxed{\Gamma \vdash \text{imperp} \Rightarrow B}$$

$$\frac{\Gamma(\text{unitid}) = B'}{\Gamma \vdash \text{unitid} \Rightarrow B'} \quad (72)$$

$$\frac{\text{Inter } B \vdash \text{topspec} \Rightarrow UI \quad U(\text{unitid}) = B'}{B, U \vdash \text{unitid} : \text{intf topspec end} \Rightarrow B' \downarrow UI} \quad (73)$$

$$\frac{\Gamma \vdash \text{imperp} \Rightarrow B \quad \Gamma \vdash \text{imperp}' \Rightarrow B'}{\Gamma \vdash \text{imperp imperp}' \Rightarrow B + B'} \quad (74)$$

$$\boxed{IB \vdash \text{topspec} \Rightarrow UI}$$

$$\frac{IB \vdash \text{spec} \Rightarrow I}{IB \vdash \text{spec} \Rightarrow \{\}, I} \quad (75)$$

$$\frac{IB \vdash \text{funspec} \Rightarrow FI}{IB \vdash \text{functor funspec} \Rightarrow FI, (\{\}, \{\}, \{\})} \quad (76)$$

$$\frac{IB \vdash \text{topspec} \Rightarrow FI, I \quad IB + I \vdash \text{topspec}' \Rightarrow FI', I'}{IB \vdash \text{topspec topspec}' \Rightarrow FI + FI', I + I'} \quad (77)$$

$$\boxed{IB \vdash \text{funspec} \Rightarrow FI}$$

$$\frac{\begin{array}{c} IB \vdash \text{sigexp} \Rightarrow I \quad IB + \{\text{strid} \mapsto I\} \vdash \text{sigexp}' \Rightarrow I' \\ FI := \{\text{funid} \mapsto (I, I')\} \quad \langle IB \vdash \text{funspec} \Rightarrow FI' \rangle \end{array}}{IB \vdash \text{funid}(\text{strid} : \text{sigexp}) : \text{sigexp}' \langle \text{and funspec} \rangle \Rightarrow FI \langle +FI' \rangle} \quad (78)$$

E TD Linking Rules

TD defines the initial static basis $B_0 = T_0, F_0, G_0, E_0$ [16, Appendix C]. We define the basis interface

$$\Upsilon_{\text{basis}} = (T_0)(F_0, G_0, E_0)$$

and assume the unit identifier **basis** may not appear in source units.

$$\boxed{\Gamma \vdash L \text{ ok}}$$

$$\frac{\begin{array}{c} \Gamma \vdash \text{uspecs ok} \\ \Gamma + \text{uspecs} \vdash \text{exps ok} \\ \text{uspecs} = \text{unitid}_1:\Upsilon_1, \dots, \text{unitid}_n:\Upsilon_n \\ \text{unitid}_1, \dots, \text{unitid}_n \text{ are distinct} \end{array}}{\Gamma \vdash \text{uspecs} \rightarrow \text{exps ok}} \quad (79)$$

$$\boxed{\Gamma \vdash \text{exps ok}}$$

$$\frac{\Gamma \vdash \text{udecs} : \text{uspecs}}{\Gamma \vdash \text{udecs} : \text{uspecs ok}} \quad (80)$$

$$\frac{\Gamma \vdash (T)(F, G, E) : \text{Sig}}{\Gamma \vdash T, F, G, E \text{ ok}} \quad (81)$$

$$\boxed{L \Rightarrow \text{udecs}}$$

$$\frac{}{\cdot \rightarrow \text{udecs} : \text{uspecs} \Rightarrow \text{udecs}} \quad (82)$$

$$\frac{\Upsilon \equiv \Upsilon_{\text{basis}} \text{ using } \varphi}{\text{basis}:\Upsilon \rightarrow \text{udecs} : \text{uspecs} \Rightarrow \text{udecs}} \quad (83)$$

$$\boxed{\Gamma \vdash L ++ L' \Rightarrow L''}$$

$$\frac{\Gamma \vdash \text{exps} ++ \text{exps}' \Rightarrow \text{exps}''}{\Gamma \vdash (\text{uspecs}_0 \rightarrow \text{exps}) ++ (\cdot \rightarrow \text{exps}') \Rightarrow \text{uspecs}_0 \rightarrow \text{exps}''} \quad (84)$$

$$\begin{array}{c}
unitid \in \text{dom}(uspecs) \\
(\Gamma + uspecs_0 + uspecs)(unitid) = T', F', G', E' \\
F', G', E' : \Upsilon \text{ using } \varphi \\
L' := \varphi(uspecs_1 \rightarrow udecs' : uspecs') \\
\Gamma \vdash (uspecs_0 \rightarrow udecs : uspecs) ++ L' \Rightarrow L'' \\
\hline
\Gamma \vdash (uspecs_0 \rightarrow udecs : uspecs) ++ (unitid : \Upsilon, uspecs_1 \rightarrow udecs' : uspecs') \Rightarrow L''
\end{array} \tag{85}$$

$$\begin{array}{c}
unitid \notin \text{dom}(exprs) \\
uspecs_0 = uspecs'', unitid : \Upsilon', uspecs''' \\
\Upsilon' \equiv \Upsilon \text{ using } \varphi \\
L' := \varphi(uspecs_1 \rightarrow exprs') \\
\Gamma \vdash (uspecs_0 \rightarrow exprs) ++ L' \Rightarrow L'' \\
\hline
\Gamma \vdash (uspecs_0 \rightarrow exprs) ++ (unitid : \Upsilon, uspecs_1 \rightarrow exprs') \Rightarrow L''
\end{array} \tag{86}$$

$$\begin{array}{c}
unitid \notin \text{dom}(exprs) \cup \text{dom}(uspecs_0) \\
\Upsilon' \equiv \Upsilon \text{ using } \varphi \\
\Gamma + \text{BT}(uspecs_0) \vdash \Upsilon' : \text{Sig} \\
L := uspecs_0, unitid : \Upsilon' \rightarrow exprs \\
L' := \varphi(uspecs_1 \rightarrow exprs') \\
\Gamma \vdash L ++ L' \Rightarrow L'' \\
\hline
\Gamma \vdash (uspecs_0 \rightarrow exprs) ++ (unitid : \Upsilon, uspecs_1 \rightarrow exprs') \Rightarrow L''
\end{array} \tag{87}$$

$$\boxed{\Gamma \vdash exprs ++ exprs' \Rightarrow exprs''}$$

$$\frac{}{\Gamma \vdash (udecs : uspecs) ++ (udecs' : uspecs') \Rightarrow udecs, udecs' : uspecs, uspecs'} \tag{88}$$

$$\frac{}{\Gamma \vdash B ++ B' \Rightarrow B + B'} \tag{89}$$

F TD Elaboration Rules

We define the bound type names in a linkset, $\text{BT}(L)$, by

$$\begin{aligned}
\text{BT}(uspecs_0 \rightarrow udecs : uspecs) &= \text{BT}(uspecs_0) \cup \text{BT}(uspecs) \\
\text{BT}(uspecs_0 \rightarrow B) &= \text{BT}(uspecs_0).
\end{aligned}$$

$$\boxed{project \Rightarrow L}$$

$$\frac{}{\cdot \Rightarrow \cdot \rightarrow \cdot} \tag{90}$$

$$\begin{array}{c}
\text{project} \Rightarrow L \quad \Upsilon_{\text{basis}} = (T)(F, G, E) \quad T \cap \text{BT}(L) = \emptyset \\
L = \text{uspecs}_0 \rightarrow \text{udecs} : \text{uspecs} \\
\Gamma := ((T, F, G, E), \{\}) + \text{BT}(\text{uspecs}_0) + \text{uspecs} \\
\Gamma \vdash \text{srcunit} \Rightarrow \text{uspecs}_1 \rightarrow \text{udecs}' : \text{uspecs}' \\
\vdash L ++ (\text{basis} : \Upsilon_{\text{basis}}, \text{uspecs}_1 \rightarrow \text{udecs}' : \text{uspecs}') \Rightarrow L'' \\
\hline
\text{project}, \text{srcunit} \Rightarrow L''
\end{array} \tag{91}$$

$$\begin{array}{c}
\text{project} \Rightarrow L \\
\text{BT}(L) \cap \text{BT}(L') = \emptyset \quad \vdash L' \text{ ok} \quad \vdash L ++ L' \Rightarrow L'' \\
\hline
\text{project}, L' \Rightarrow L''
\end{array} \tag{92}$$

$$\boxed{\Gamma \vdash \text{srcunit} \Rightarrow L}$$

$$\begin{array}{c}
\text{srcunit} = (\text{unit } \text{unitid} = \text{unit } \text{topdec } \text{end}) \\
\Gamma \vdash \text{topdec} \Rightarrow \text{uspecs}_0 \rightarrow T, F, G, E \\
\hline
\Gamma \vdash \text{srcunit} \Rightarrow \text{uspecs}_0 \rightarrow \text{srcunit} : \text{unitid} : (T)(F, G, E)
\end{array} \tag{93}$$

$$\boxed{\Gamma \vdash \text{topdec} \Rightarrow L}$$

$$\begin{array}{c}
\Gamma \vdash \text{impexp} \Rightarrow L \\
\hline
\Gamma \vdash \text{import } \text{impexp} \Rightarrow L
\end{array} \tag{94}$$

$$\begin{array}{c}
\text{topdec} = \text{strdec} \quad \Gamma \vdash \text{topdec} : B \\
\hline
\Gamma \vdash \text{topdec} \Rightarrow \cdot \rightarrow B
\end{array} \tag{95}$$

$$\begin{array}{c}
\text{topdec} = \text{sigdec} \quad \Gamma \vdash \text{topdec} : B \\
\hline
\Gamma \vdash \text{topdec} \Rightarrow \cdot \rightarrow B
\end{array} \tag{96}$$

$$\begin{array}{c}
\text{topdec} = \text{fundec} \quad \Gamma \vdash \text{fundec} : B \\
\hline
\Gamma \vdash \text{topdec} \Rightarrow \cdot \rightarrow B
\end{array} \tag{97}$$

$$\begin{array}{c}
\Gamma \vdash \text{topdec} \Rightarrow \text{uspecs}_0 \rightarrow B \\
\Gamma + \text{BT}(\text{uspecs}_0) + B \vdash \text{topdec}' \Rightarrow L \\
B' := T \text{ of } B, \{\}, \{\}, E_\emptyset \quad \Gamma \vdash (\text{uspecs}_0 \rightarrow B') ++ L \Rightarrow L' \\
\hline
\Gamma \vdash \text{local } \text{topdec in } \text{topdec}' \text{ end} \Rightarrow L'
\end{array} \tag{98}$$

$$\begin{array}{c}
\Gamma \vdash \text{topdec} \Rightarrow L \quad L = \text{uspecs}_0 \rightarrow B \\
\Gamma + \text{BT}(\text{uspecs}_0) + B \vdash \text{topdec}' \Rightarrow L' \\
\Gamma \vdash L ++ L' \Rightarrow L'' \\
\hline
\Gamma \vdash \text{topdec } \text{topdec}' \Rightarrow L''
\end{array} \tag{99}$$

$$\boxed{\Gamma \vdash \textit{impexp} \Rightarrow L}$$

$$\frac{\Gamma(\textit{unitid}) = T, F, G, E \quad L := \textit{unitid}:(T)(F, G, E) \rightarrow \emptyset, F, G, E}{\Gamma \vdash \textit{unitid} \Rightarrow L} \quad (100)$$

$$\frac{\Gamma \vdash \textit{intexp} \Rightarrow (T)(F, G, E) \quad L := \textit{unitid}:(T)(F, G, E) \rightarrow \emptyset, F, G, E}{\Gamma \vdash \textit{unitid} : \textit{intexp} \Rightarrow L} \quad (101)$$

$$\frac{\begin{array}{l} \Gamma \vdash \textit{impexp} \Rightarrow L \quad \Gamma \vdash \textit{impexp}' \Rightarrow L' \\ \text{BT}(L) \cap \text{BT}(L') = \emptyset \quad \Gamma \vdash L \mathbin{++} L' \Rightarrow L'' \end{array}}{\Gamma \vdash \textit{impexp} \textit{ impexp}' \Rightarrow L''} \quad (102)$$

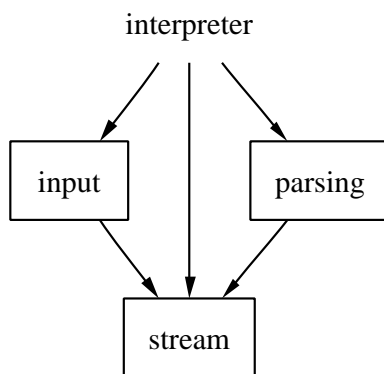


Figure 22: The dependencies among the libraries and the Mini-ML interpreter. The interpreter uses all three libraries and the input and parsing libraries use the stream library.

G Larger Example: Mini-ML

In this appendix we present a more realistic example based on code used in Frank Pfenning’s 1995 *Logic Programming* course at Carnegie Mellon. The example comprises a stream library, an input library, a parsing library, and an interpreter for Mini-ML that uses the libraries.⁹ In Figure 22, we illustrate the high-level dependencies in the code. We first discuss the library handoff units and the interpreter. Then we give library implementations and show how to link these to make an executable interpreter.

Stream Library

The stream library declares a signature `BASIC_STREAM` describing the visible “core” of functional streams, a signature `STREAM` that extends `BASIC_STREAM`, a functor `Stream` that lifts a `BASIC_STREAM` structure to `STREAM`, and plain and memoizing `STREAM` structures. The handoff unit `StreamLib` declares all of these components by importing units `STREAMSIG` and `StreamImpl`:

```

unit STREAMSIG =
unit
  signature BASIC_STREAM =
    sig
      type 'a stream
      val empty : 'a stream
      val create : (unit -> ('a * 'a stream) option) -> 'a stream
      val expose : 'a stream -> ('a * 'a stream) option
      (* ... *)
    end
end

```

⁹The libraries were written by Chris Okasaki, Frank Pfenning, and Bob Harper. Pfenning wrote the interpreter to demonstrate the libraries to teams of students using them to implement compilers for the course.

```

signature STREAM =
  sig
    include BASIC_STREAM
    exception Empty
    val delay : (unit -> 'a stream) -> 'a stream
    val lcons : 'a * (unit -> 'a stream) -> 'a stream
    val cons : 'a * 'a stream -> 'a stream
    val map : ('a -> 'b) -> 'a stream -> 'b stream
    (* ... *)
  end
end

unit StreamLib =
unit
  import STREAMSIG

  import StreamImpl :
  intf
    functor Stream(structure BasicStream : BASIC_STREAM)
      : STREAM where type 'a stream = 'a BasicStream.stream

    structure PlainStream : STREAM

    structure MemoStream : STREAM

    (* default stream package memoizes *)
    structure Stream : STREAM
      where type 'a stream = 'a MemoStream.stream
    end
end
end

```

The other libraries and the interpreter import `StreamLib`, heavily using its structure `Stream : STREAM` but never directly referring to the auxiliary units `STREAMSIG` and `StreamImpl`. We shall give a stream implementation `StreamImpl` that, like `StreamLib`, performs IC against `STREAMSIG` to obtain the stream signatures.

Input Library

The input library declares a signature `INPUT` and structure `Input : INPUT`. Structure `Input` creates character streams. For example, the function

```
Input.promptkeybd : string -> char Stream.stream
```

creates a stream that, when forced, prompts the user for input a line at a time. The handoff unit `InputLib` imports units `INPUTSIG` and `InputImpl`:

```

unit INPUTSIG =
unit
  local
    import StreamLib
  in
    signature INPUT =
      sig
        val readfile   : string -> char Stream.stream
        val readkeybd  : unit    -> char Stream.stream
        val promptkeybd : string -> char Stream.stream
      end
    end
end

unit InputLib =
unit
  import INPUTSIG

  import InputImpl :
    intf
    structure Input : INPUT
  end
end

```

This description of the input library performs IC against the stream library *description* (unit `StreamLib`) but SC against its *implementation* (unit `StreamImpl`).

Parsing Library

The parsing library declares a signature `POS` and structure `Pos : POS` for positions within a file, a signature `BASIC_PARSING` describing a “core” set of parsing combinators, a signature `PARSING` that extends `BASIC_PARSING` with additional combinators and utilities, a functor `Parsing` that lifts a `BASIC_PARSING` structure to `PARSING`, and, finally, a structure `Parsing : PARSING`. The parsing library uses the stream library. For example,

```
Pos.markstream : char Stream.stream -> (char * Pos.pos) Stream.stream
```

marks a character stream with position information for reporting errors and

```

Parsing.transform : ('a,'t) Parsing.parser
  -> ('t * Pos.pos) Stream.stream
  -> 'a Stream.stream

```

creates a stream of parsed values from a parser and a marked stream of tokens. Following the now familiar pattern, the handoff unit `ParsingLib` imports `PARSINGSIG` and `ParsingImpl`:

```

unit PARSINGSIG =
unit
  local
    import StreamLib
  in
    signature POS =
      sig
        type pos
        val markstream : char Stream.stream -> (char * pos) Stream.stream
        (* ... *)
        val makestring : pos -> string
      end

    signature BASIC_PARSING =
      sig
        (* Parser with token type 't, result type 'a *)
        type ('a,'t) parser
        type pos (* = Pos.pos *)

        val succeed : 'a -> ('a,'t) parser
        val fail : ('a,'t) parser

        val done : 'a -> ('a,'t) parser
        val any : ('t,'t) parser

        val -- : ('a,'t) parser * ('a -> ('b,'t) parser) -> ('b,'t) parser
        val ## : ('a,'t) parser * (pos -> ('a,'t) parser) -> ('a,'t) parser

        val !! : ('a,'t) parser -> ('a * pos,'t) parser

        val $ : (unit -> ('a,'t) parser) -> ('a,'t) parser

        val lookahead : ('a,'t) parser -> ('a -> ('b,'t) parser)
          -> ('b,'t) parser
        val parse : ('a,'t) parser -> ('t * pos) Stream.stream -> 'a option
        val transform : ('a,'t) parser -> ('t * pos) Stream.stream
          -> 'a Stream.stream
      end

    signature PARSING =
      sig
        include BASIC_PARSING
        val && : ('a,'t) parser * ('b,'t) parser -> ('a * 'b,'t) parser
        val || : ('a,'t) parser * ('a,'t) parser -> ('a,'t) parser
        (* ... many more ... *)
      end
    end
  end
end

```

```

unit ParsingLib =
unit
  import PARSINGSIG

  import ParsingImpl :
    intf
      structure Pos : POS

      functor Parsing(structure BasicParsing : BASIC_PARSING
        where type pos = Pos.pos)
        : PARSING
        where type 'a parser = 'a BasicParsing.parser
        where type pos = Pos.pos

      structure Parsing : PARSING
        where type pos = Pos.pos
    end
end
end

```

Interpreter

The Mini-ML interpreter is a single unit `Interp` that declares signatures and structures for identifiers, lexical tokens, an abstract syntax, lexical analysis, parsing, evaluation, and a read-eval-print loop. It performs IC against the handoff units `StreamLib`, `InputLib`, and `ParsingLib` and units `BoolLib` and `IntLib` (not shown but assumed to declare the Standard Basis Library structures `Bool` and `Int`).

```

unit Interp =
unit
  import StreamLib InputLib ParsingLib IntLib BoolLib

  signature ID =
    sig
      eqtype id

      val id : string -> id
      val eq : id * id -> bool
      val makestring : id -> string
    end

  structure Id :> ID = struct (* ... *) end
end

```



```

signature TOKEN =
  sig
    datatype token =
      Id of Id.id
    | Num of int
    | LParen | RParen
    | Plus | Times | Neg | Eql
    | Fn | Rec | Is
    | LAngle | RAngle | Comma | Fst | Snd
    | True | False | If | Then | Else
    | Let | Be | In
    | Semi

    val makestring : token -> string
  end

structure Token :> TOKEN = struct (* ... *) end

signature ABSSYN =
  sig
    datatype abssyn =
      Var of Id.id
    | Num of int
    | Bool of bool
    (* ... *)

    val makestring : abssyn -> string
  end

structure AbsSyn :> ABSSYN = struct (* ... *) end

signature LEXER =
  sig
    val lex_item : (Token.token * Pos.pos, char) Parsing.parser
    val lex :
      (char * Pos.pos) Stream.stream ->
      (Token.token * Pos.pos) Stream.stream
  end

structure Lexer :> LEXER = struct (* ... *) end

signature PARSER =
  sig
    val parse_prog : (AbsSyn.abssyn, Token.token) Parsing.parser
    val parse : (Token.token * Pos.pos) Stream.stream
      -> AbsSyn.abssyn Stream.stream
  end

structure Parser :> PARSER = struct (* ... *) end

```

```

signature EVALUATOR =
  sig
    val evaluate : AbsSyn.abssyn Stream.stream
      -> AbsSyn.abssyn Stream.stream
  end

structure Evaluator :> EVALUATOR = struct (* ... *) end

signature INTERPRETER =
  sig
    val interpreter : char Stream.stream -> string Stream.stream
    val interact : unit -> unit
  end

structure Interpreter :> INTERPRETER =
  struct
    val interpreter =
      (Stream.map AbsSyn.makestring) o
      (Evaluator.evaluate) o
      (Parser.parse) o
      (Lexer.lex) o
      (Pos.markstream)

    fun interact () =
      let
        fun loop s =
          (case Stream.expose s of
             NONE => ()
          | SOME (result, s') =>
              (print (result ^ "\n"); loop s'))
          val is = interpreter (Input.promptkeybd "> ")
        in
          loop is
        end
      end
  end
end

```

The interpreter is organized around the stream library. For example, the Mini-ML evaluator has type

```

Evaluator.evaluate : AbsSyn.abssyn Stream.stream
  -> AbsSyn.abssyn Stream.stream

```

and maps a stream of expressions into a stream of values, filtering out those expressions whose evaluation gets stuck (and printing an error message). This organization lends itself well to experimenting and debugging. A one-line change

```

val interpreter =
  (Stream.map AbsSyn.makestring) o
  (* (Evaluator.evaluate) o *)
  (Parser.parse) o
  (Lexer.lex) o
  (Pos.markstream)

```

produces a variant of the interpreter that accepts syntactically correct Mini-ML expressions and prints the resulting abstract syntax.

Library Implementations

Unit `StreamImpl` performs IC against the unit `STREAMSIG`:

```

unit StreamImpl =
unit
  import STREAMSIG

  functor Stream(structure BasicStream : BASIC_STREAM)
    :> STREAM where type 'a stream = 'a BasicStream.stream =
    struct
      open BasicStream
      exception Empty
      fun delay t = create (expose o t)
      (* ... *)
    end

  structure BasicStream :> BASIC_STREAM =
    struct (* ... *) end

  structure PlainStream : STREAM =
    Stream (structure BasicStream = BasicStream)

  structure BasicMemoStream :> BASIC_STREAM =
    struct (* ... *) end

  (* default stream package memoizes *)
  structure Stream = MemoStream
end

```

Unit `InputImpl` performs IC against `INPUTSIG` and `StreamLib` but SC against `StreamImpl`. It also needs a way to perform IO: We assume the unit `TextIOLib` (not shown) declares the Standard Basis Library structure `TextIO`.

```

unit InputImpl =
unit
  local
    import INPUTSIG StreamLib TextIOLib
  in
    structure Input :> INPUT = struct (* ... *) end
  end
end

```

We declared everything specified by `StreamLib` in `StreamImpl`. Here we demonstrate an alternative. Unit `ParsingImpl` imports structure `Pos` and functor `Parsing` from auxilliary units `PosImpl` and `ParsingFunImpl`:

```

unit PosImpl =
unit
  local
    import PARSINGSIG StreamLib
  in
    structure Pos :> POS = struct (* ... *) end
  end
end

unit ParsingFunImpl =
unit
  local
    import PARSINGSIG StreamLib PosImpl
  in
    functor Parsing (structure BasicParsing : BASIC_PARSING
                      where type pos = Pos.pos) : PARSING =
      struct (* ... *) end
    end
  end

unit ParsingImpl =
unit
  local
    import PARSINGSIG StreamLib
  in
    import PosImpl ParsingFunImpl

    structure BasicParsing :> BASIC_PARSING where type pos = Pos.pos =
      struct (* ... *) end

    structure Parsing =
      Parsing (structure BasicParsing = BasicParsing)
    end
  end
end

```

Linking

To create an executable interpreter, we shall link unit `Interp` with library implementations and the unit

```

unit RunInterp =
unit
  import Interp
  val () = Interpreter.interact()
end

```

that gets things started. We begin by compiling `Interp` and `RunInterp`:

$$L_r = \text{link}(\text{STREAMSIG}, \text{StreamLib}, \\ \text{INPUTSIG}, \text{InputLib}, \\ \text{PARSINGSIG}, \text{ParsingLib}, \\ \text{IntLib}, \text{BoolLib}, \\ \text{Interp}, \\ \text{RunInterp}).$$

We then compile the libraries:

$$L_S = \text{link}(\text{STREAMSIG}, \text{StreamImpl}) \\ L_I = \text{link}(\text{STREAMSIG}, \text{StreamLib}, \text{INPUTSIG}, \text{TextIOLib}, \text{InputImpl}) \\ L_P = \text{link}(\text{STREAMSIG}, \text{StreamLib}, \\ \text{PARSINGSIG}, \text{PosImpl}, \text{ParsingFunImpl}, \text{ParsingImpl})$$

Finally, we complete the linkset

$$L = \text{link}(L_S, L_I, L_P, L_r)$$

to an executable program. When run, the program will prompt the user, parse Mini-ML expressions from its input, evaluate these in turn, and print the resulting values (provided evaluation terminates). The following typescript demonstrates the program in action.

```
> true+3;
Run-time error arithmetic type error
> let sq be fn x in x*x in sq(sq 4);
256
> let fib be rec f(x) is
>   if x=0 then 1
>   else if x=1 then 1
>   else f(x + ~1) + f(x + ~2)
> in fib 7;
21
>
```

An implementation could avoid unnecessary recompilation, maintaining a repository of compiled code during IC. For example, such an implementation might employ

$$L_0 = \text{link}(\text{STREAMSIG}) \\ L_1 = \text{link}(L_0, \text{StreamImpl}) \\ L_2 = \text{link}(L_0, \text{StreamLib}) \\ L_3 = \text{link}(L_2, \text{INPUTSIG}, \text{TextIOLib}, \text{InputImpl}) \\ L_4 = \text{link}(L_2, \text{PARSINGSIG}, \text{PosImpl}, \text{ParsingFunImpl}, \text{ParsingImpl})$$

to avoid recompiling `STREAMSIG` and `StreamLib` while producing $L_S = L_1$, $L_I = L_3$, and $L_P = L_4$.